

Figure 8-1. A1 Display PCA

6080A-1604

Schematic Diagrams

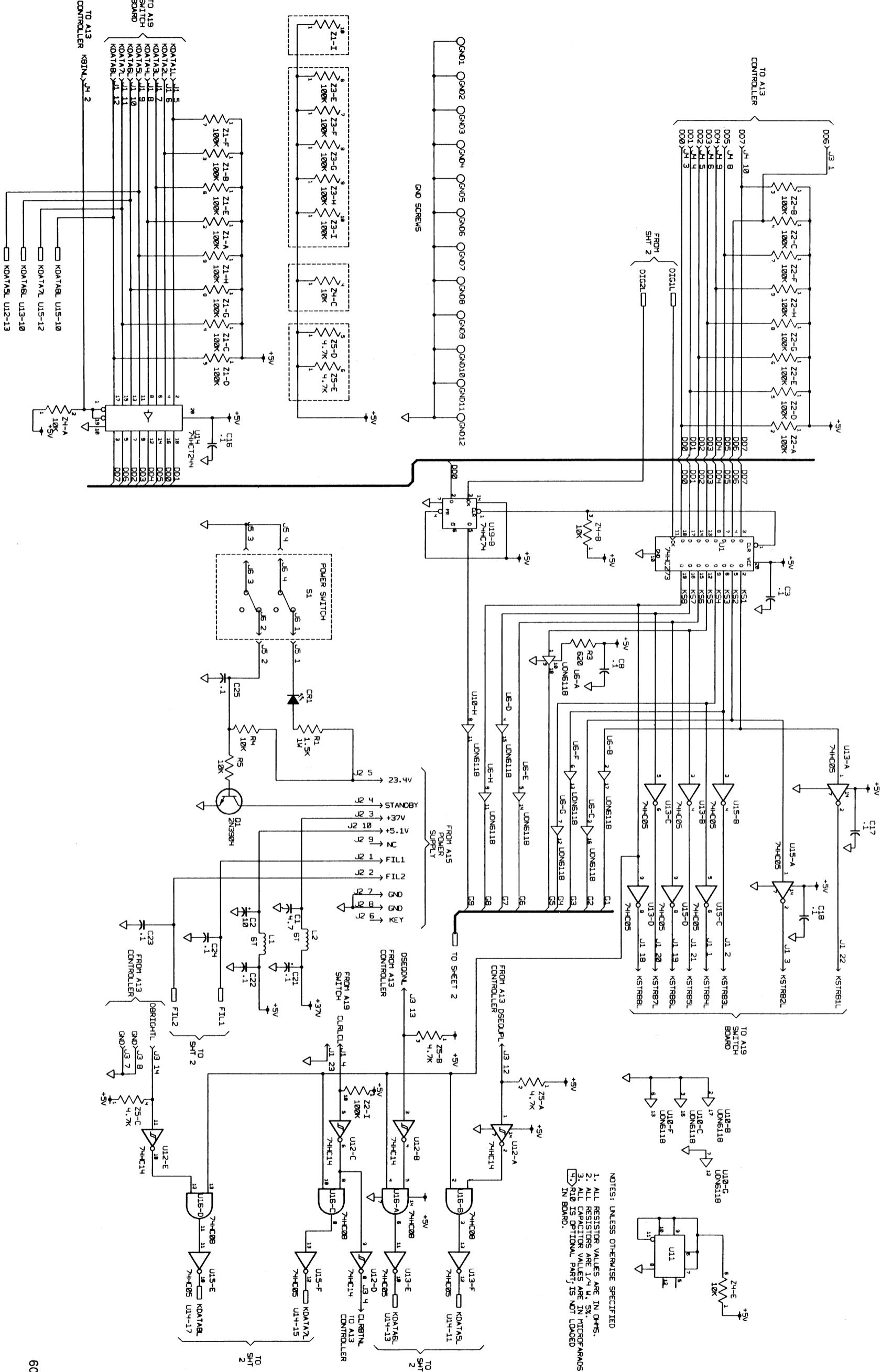


Figure 8-1. A1 Display PCA (cont)

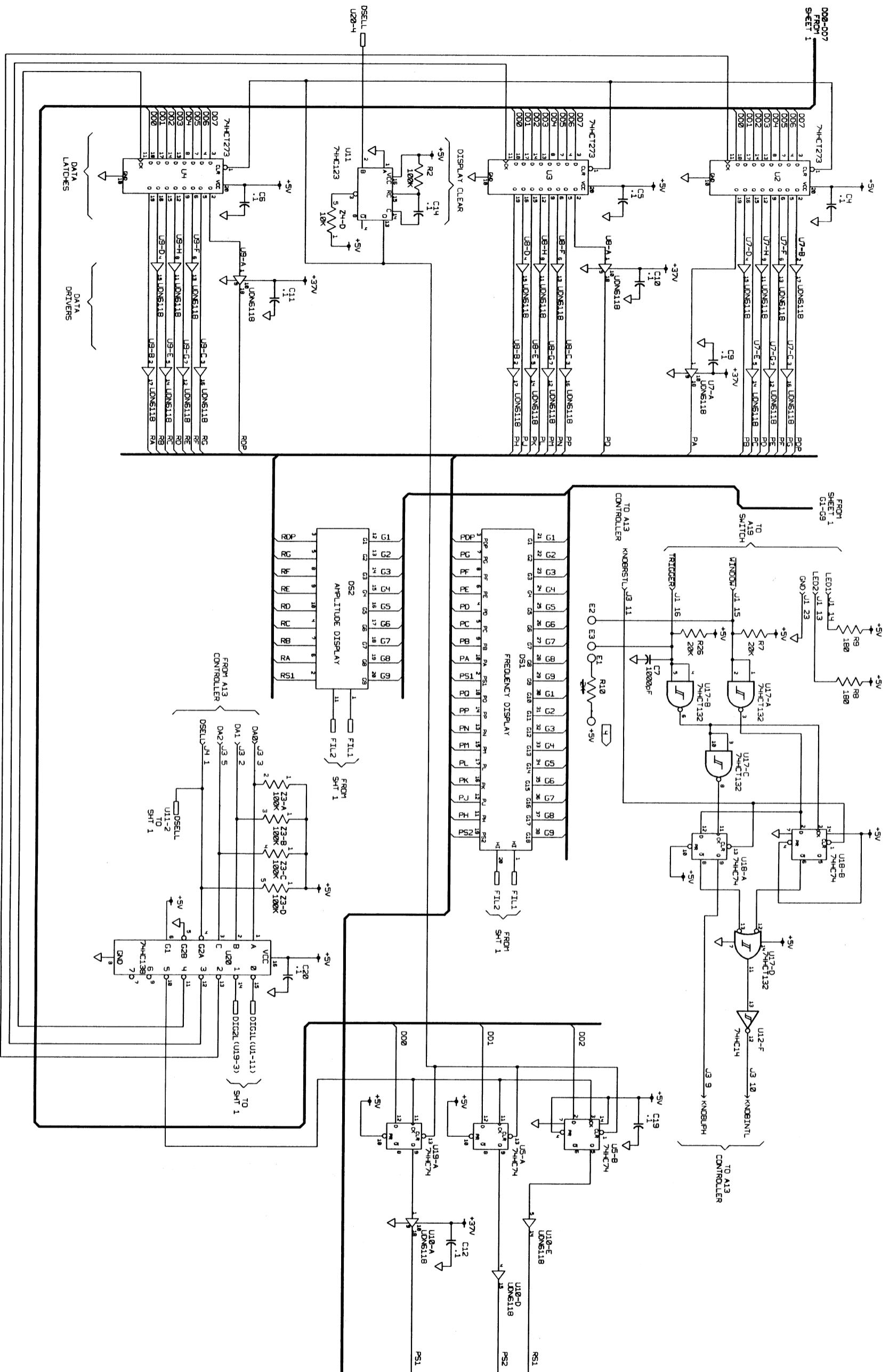
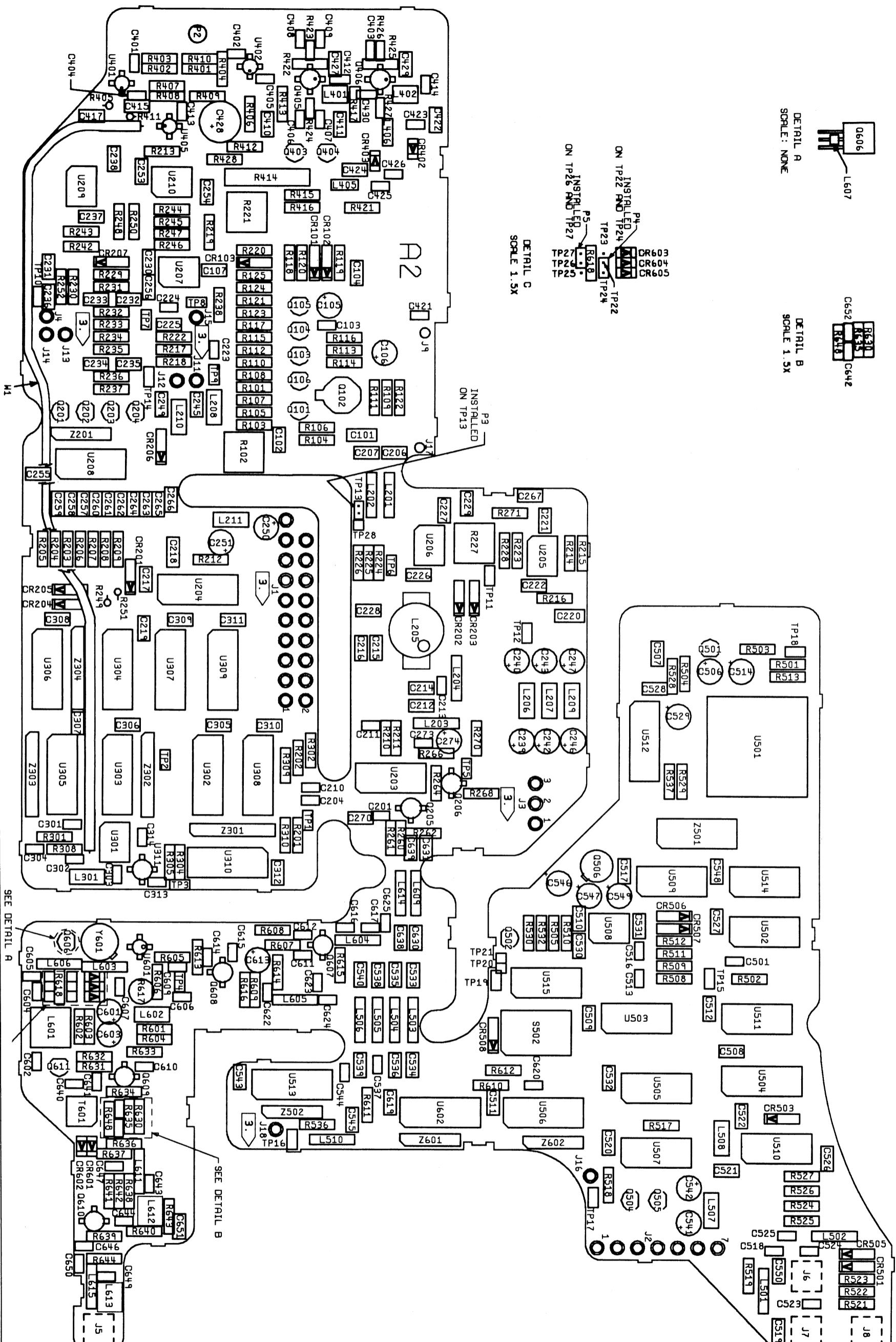
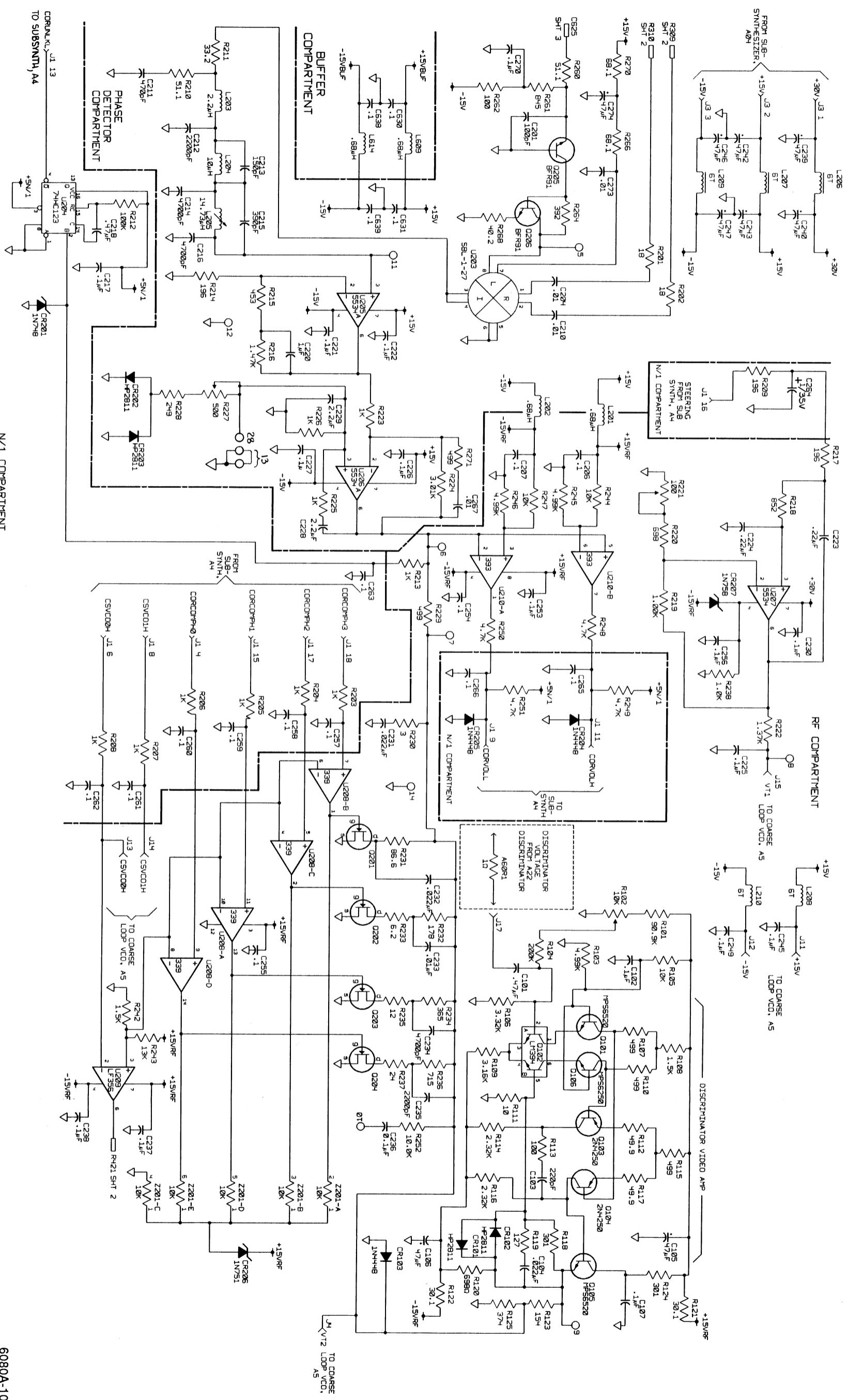


Figure 8-1. A1 Display PCA (cont)

SCHEMATIC DIAGRAMS





Schematic Diagrams

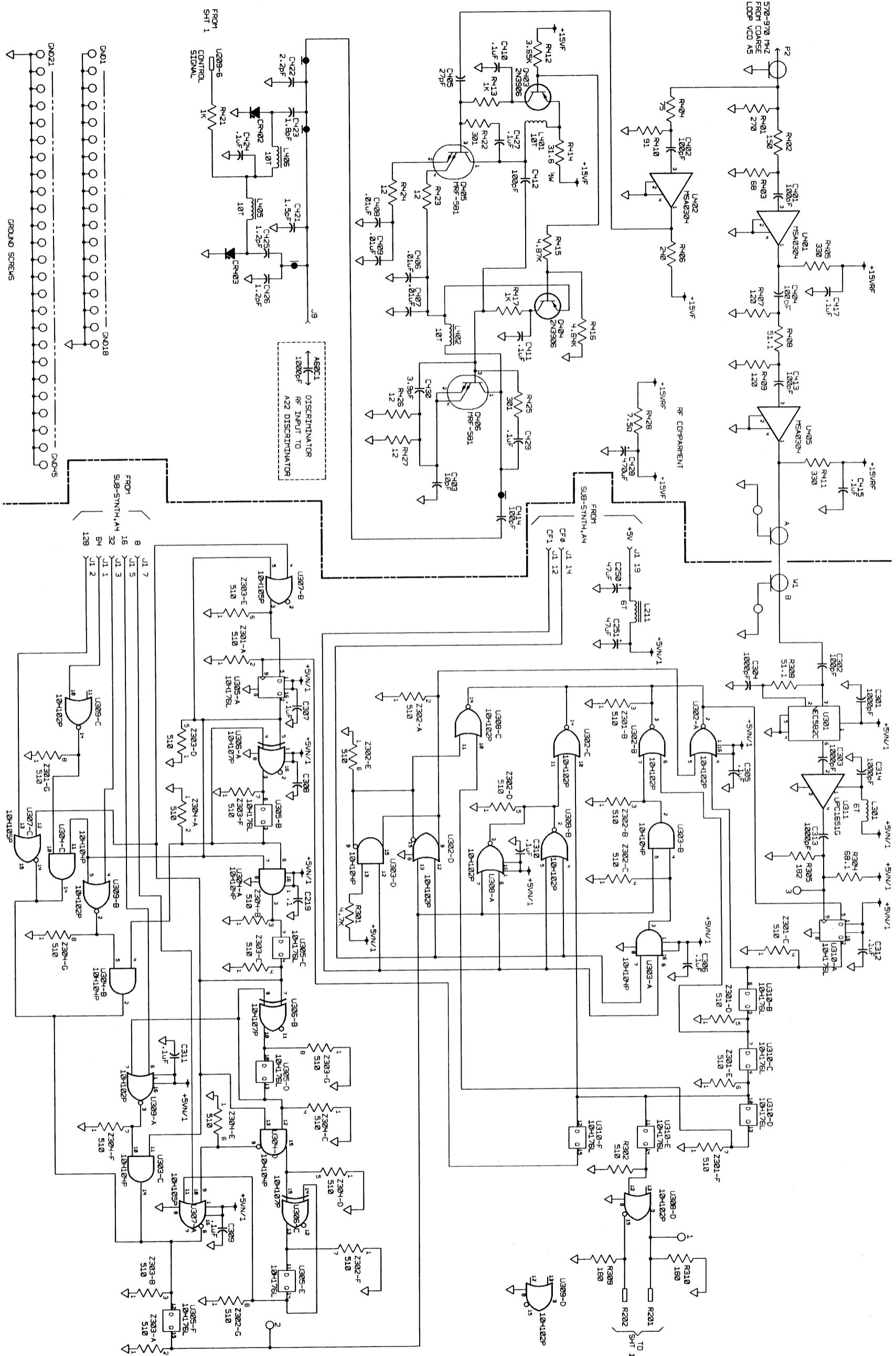
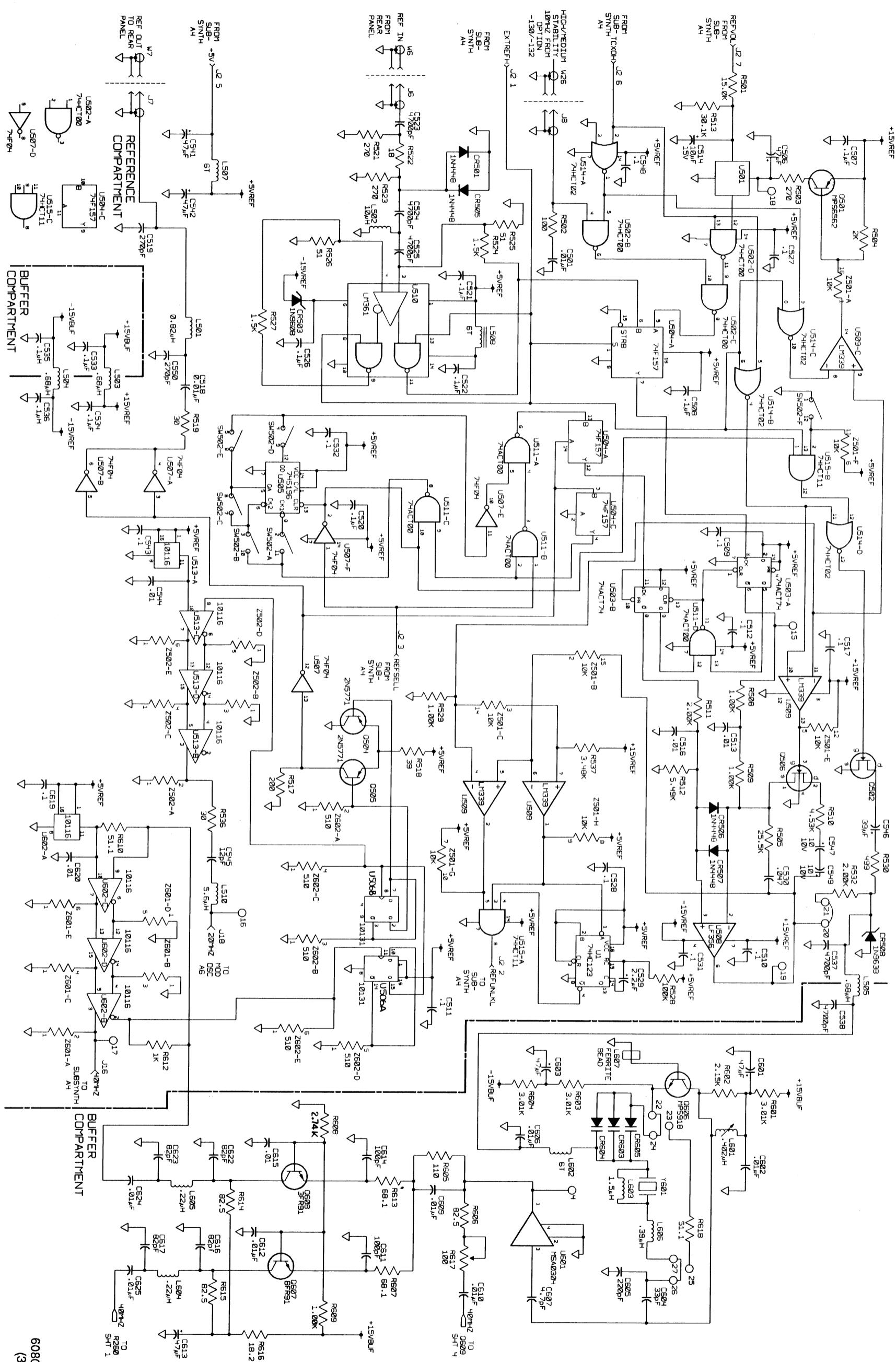


Figure 8-2. A2 Coarse Loop PCA (cont)

Figure 8-2. A2 Coarse Loop PCA (cont)

6080A-1060
(3 of 4)



Schematic Diagrams

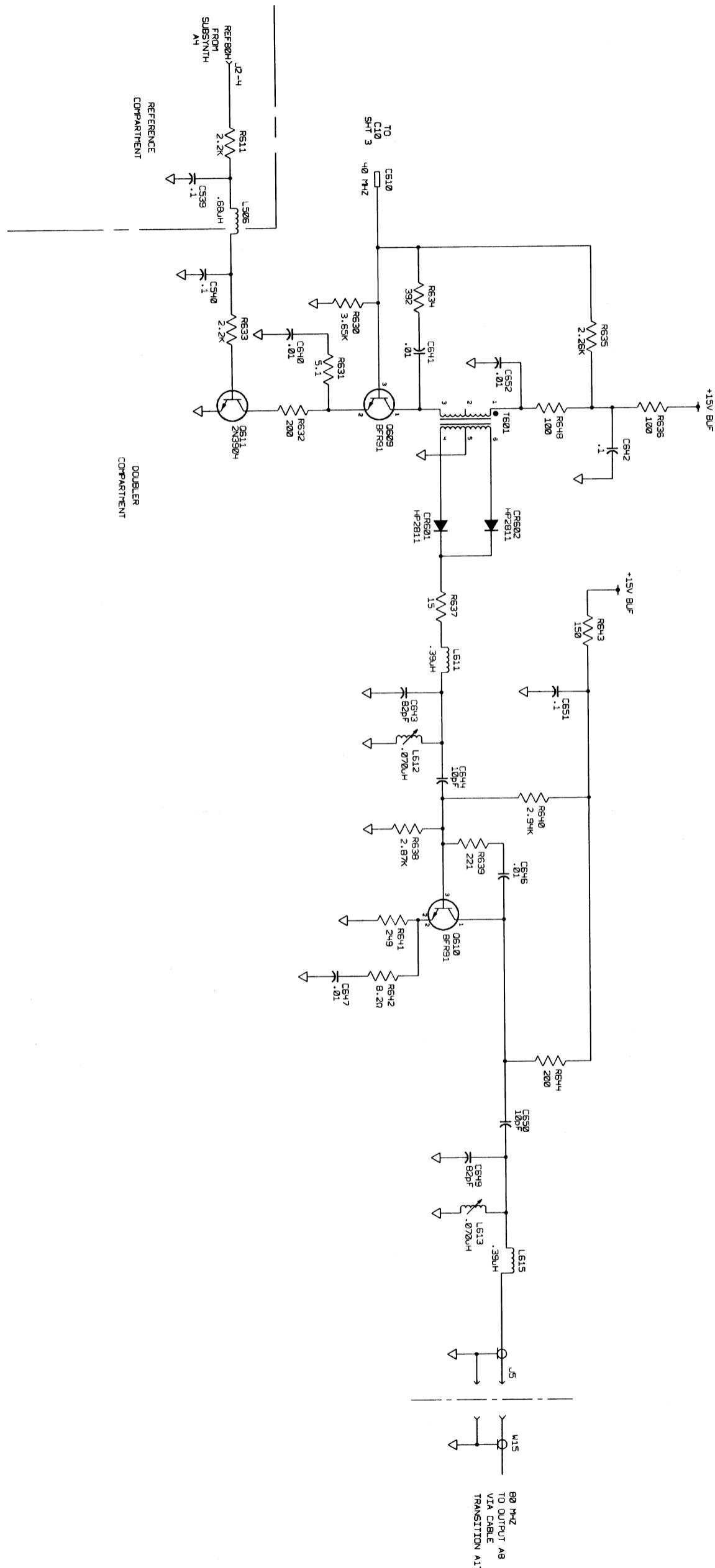
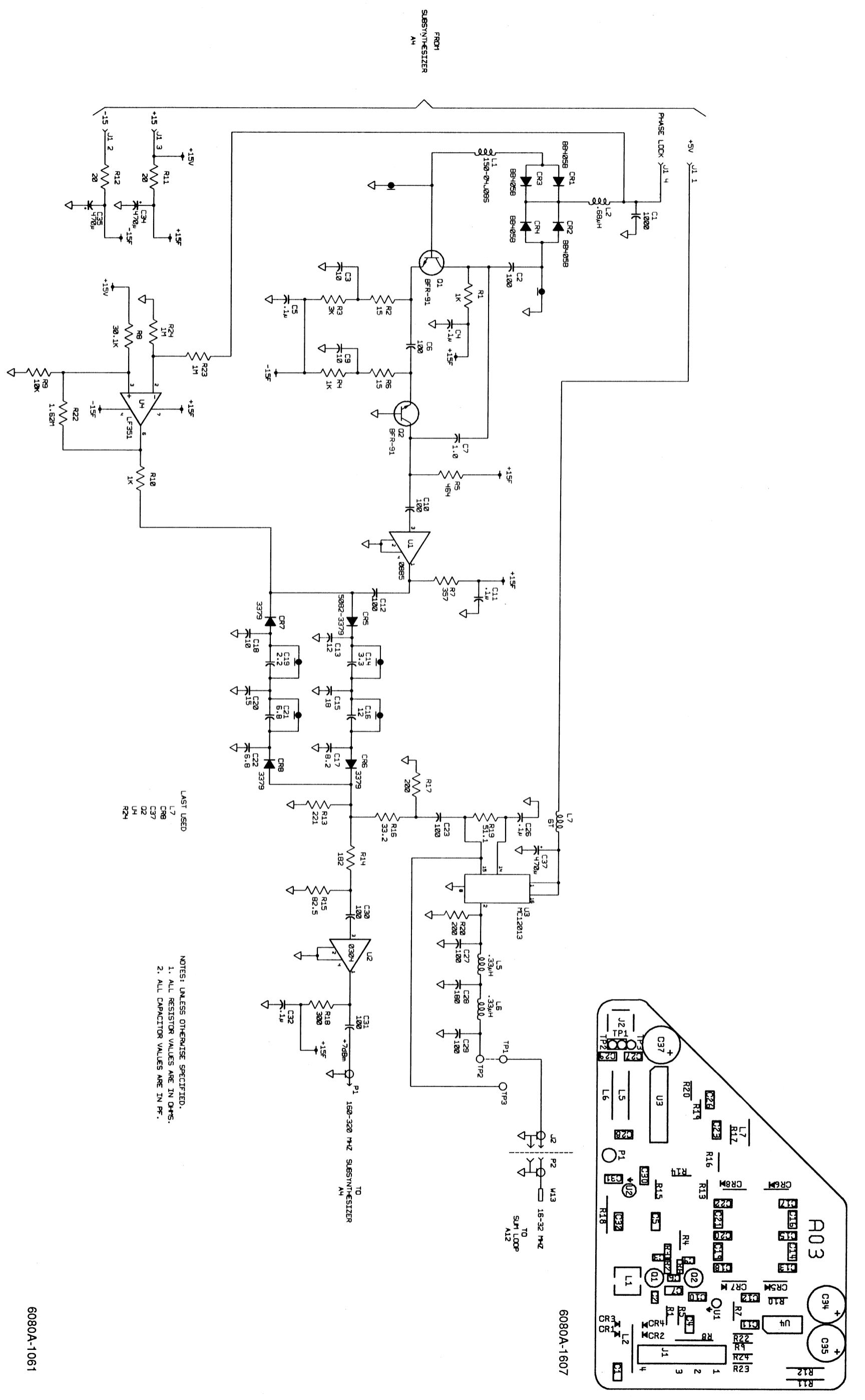


Figure 8-2. A2 Coarse Loop PCA (cont)



SCHEMATIC DIAGRAMS

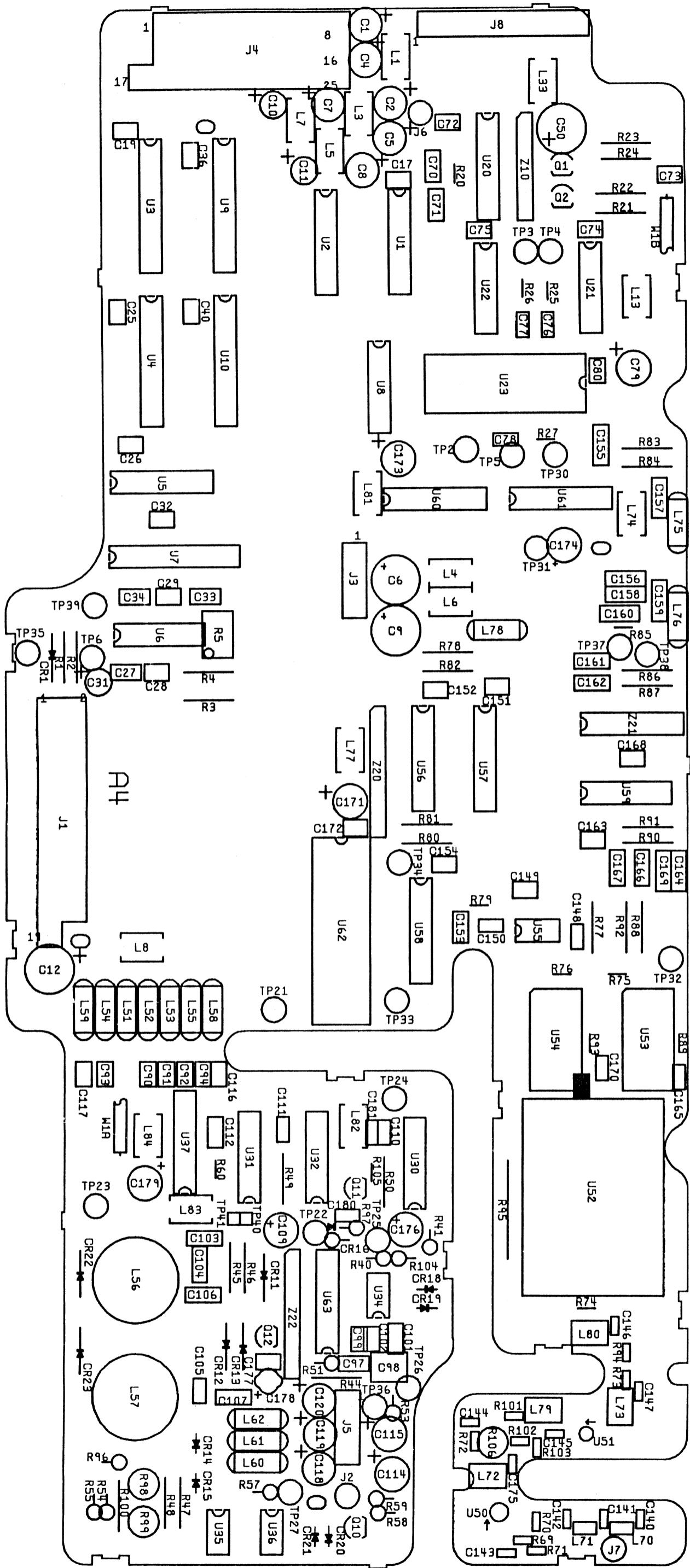
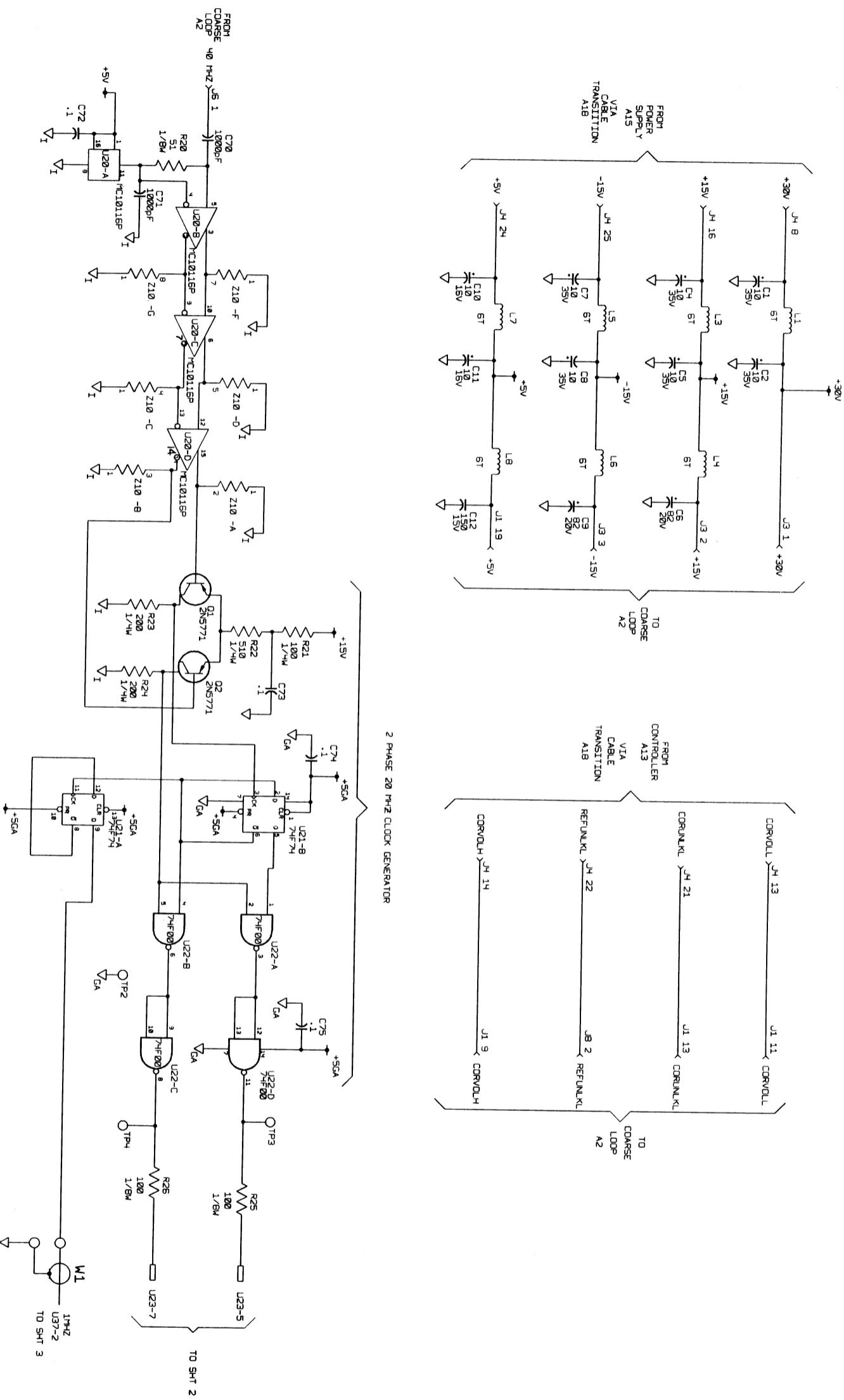


Figure 8-4. A4 Sub-Synthesizer PCA

2 PHASE 20 MHZ CLOCK GENERATOR



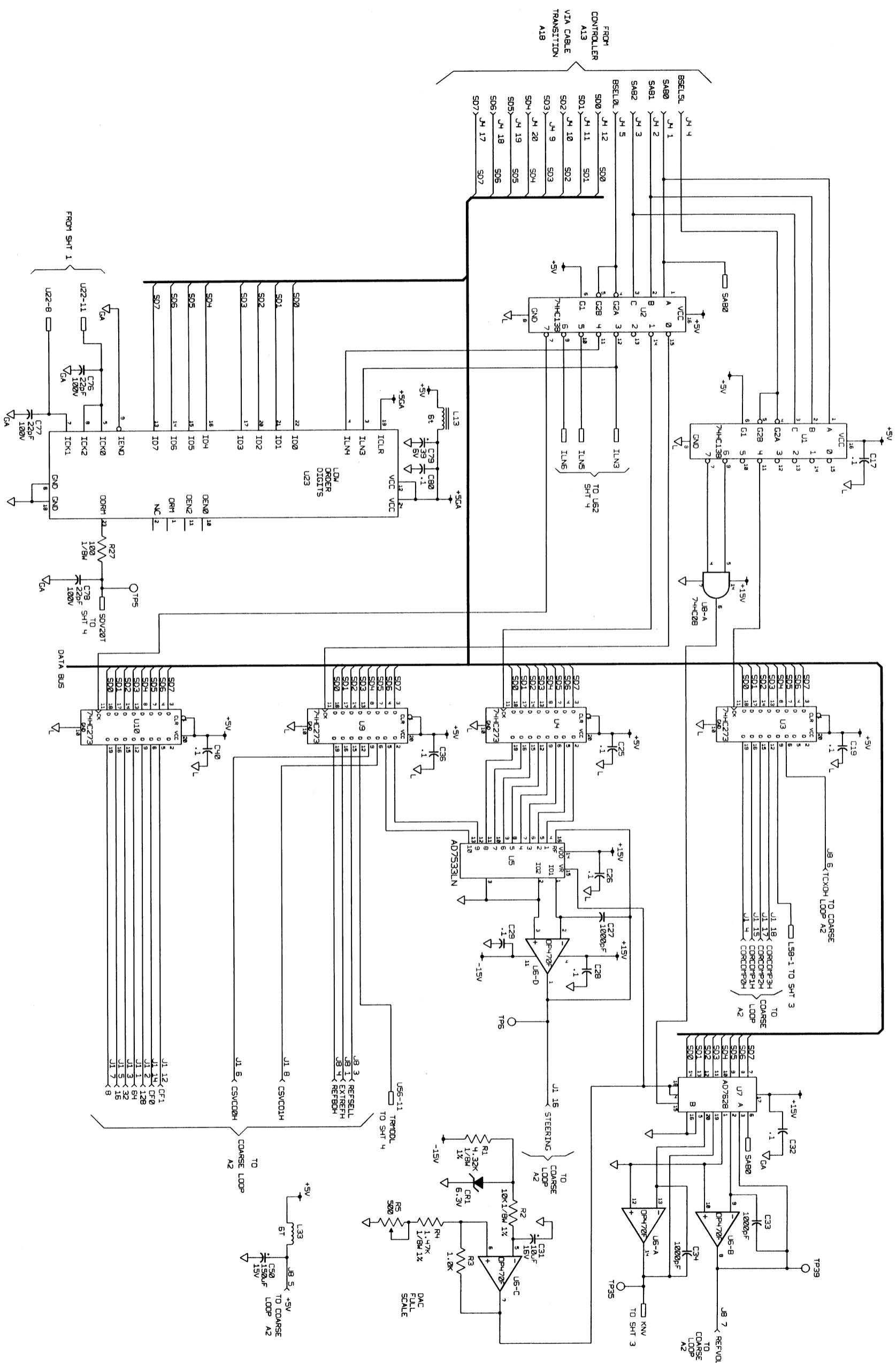
NOTES: UNLESS OTHERWISE SPECIFIED,

1. ALL RESISTORS ARE $1\frac{1}{4}\text{W}$, $\pm 5\%$.
 2. ALL CAPACITOR VALUES ARE IN MICROFARADS.
 3. ALL RESISTOR VALUES ARE IN OHMS.
 4. ALL INDUCTOR VALUES ARE IN MICROHENRIES.

Figure 8-4. A4 Sub-Synthesizer PCA (cont)

Schematic Diagrams

Figure 8-4. A4 Sub-Synthesizer PCA (cont)



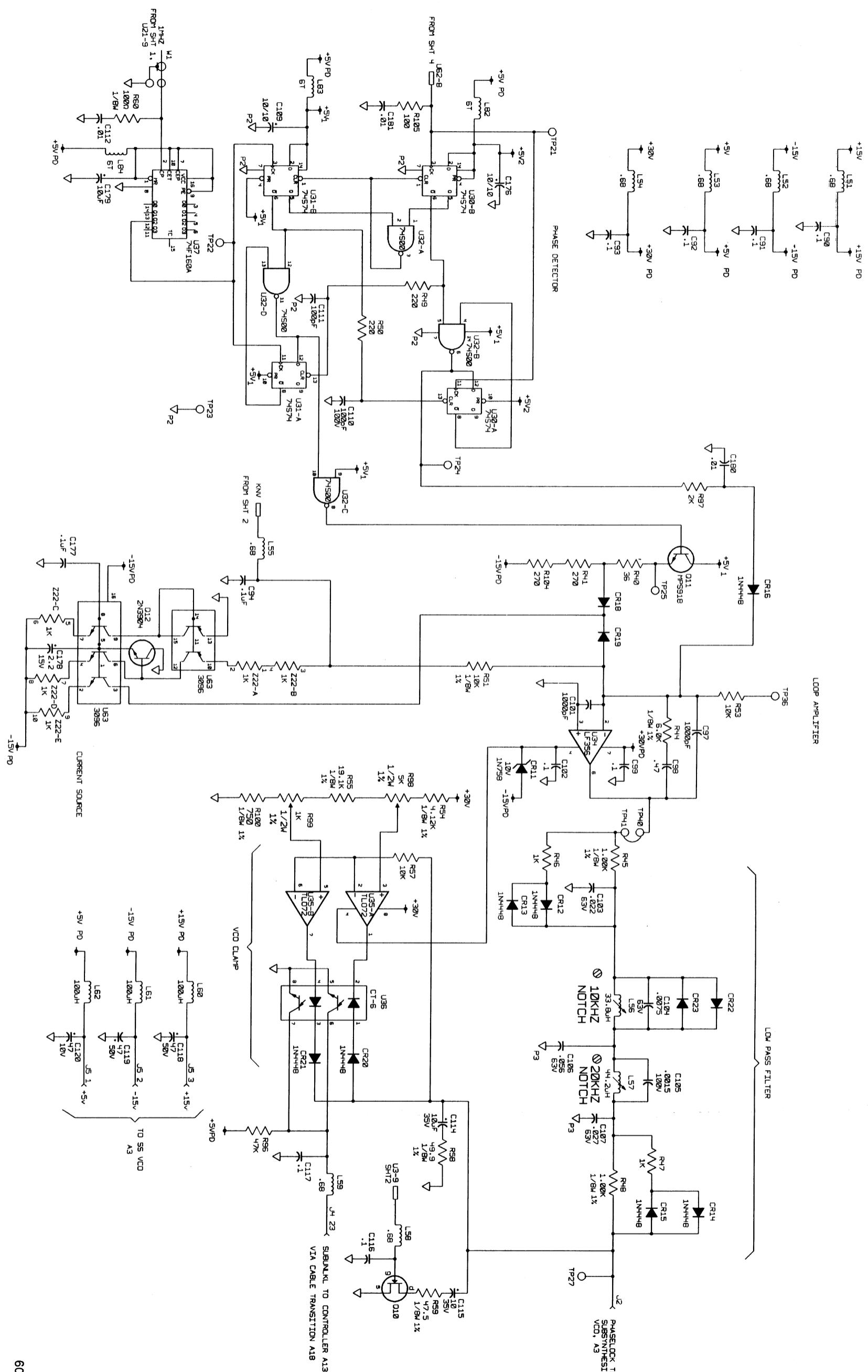
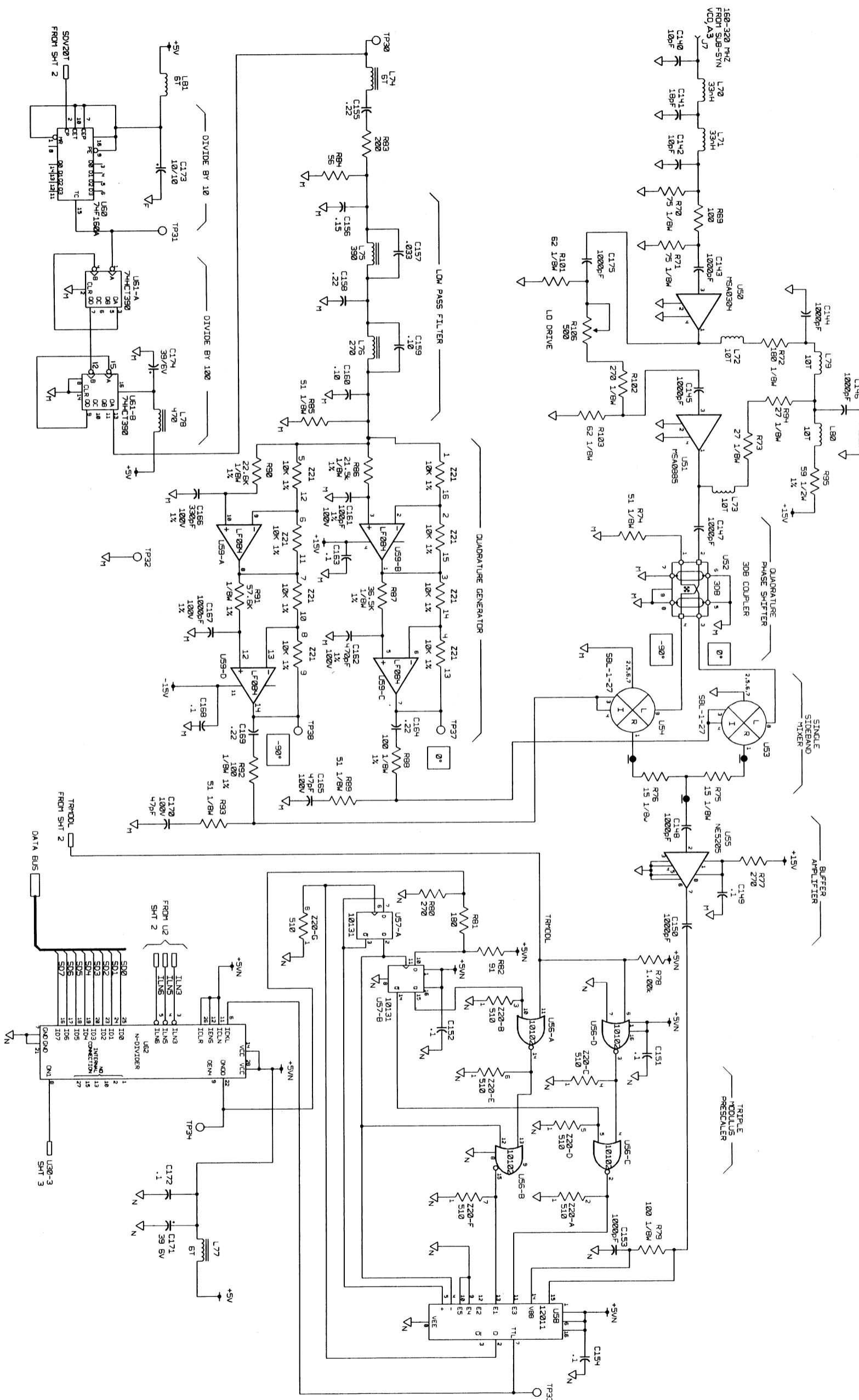
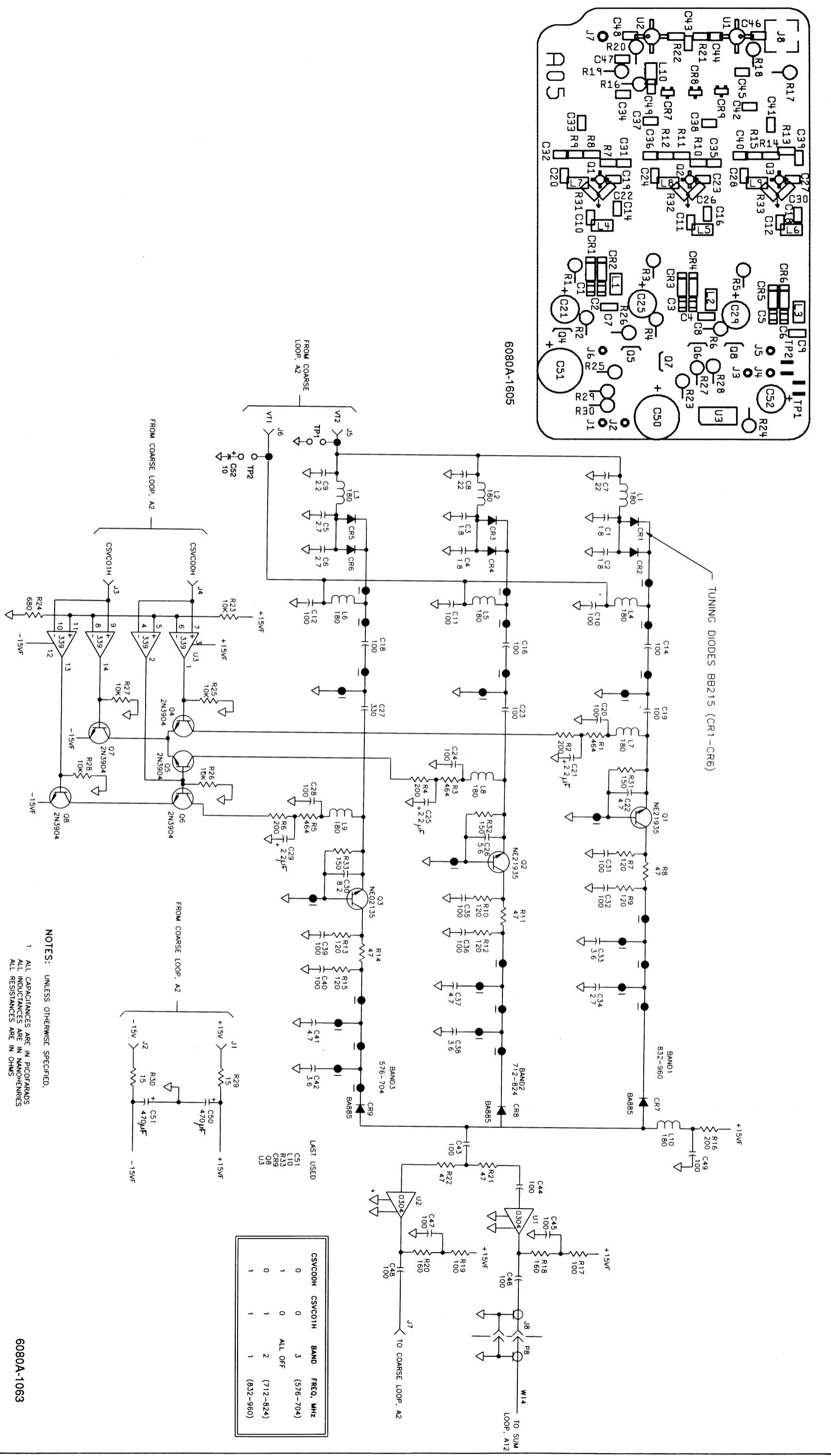


Figure 8-4. A4 Sub-Synthesizer PCA (cont.)

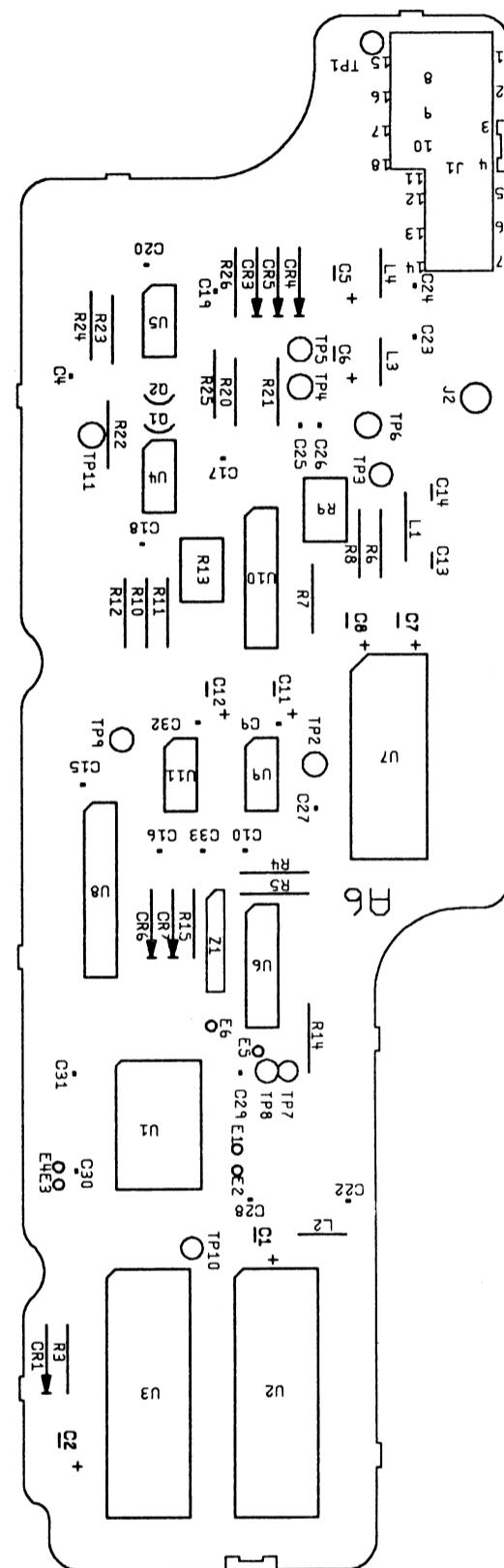
6080A-1062
(3 of 4)

SCHEMATIC DIAGRAMS





SCHEMATIC DIAGRAMS



6080A-1602

REF.	V _{CC}	+15V	-15V	#PINS	TYPE
U1	2,23,37	3,22,33	42,62	80	65022 ARRAY
U2	1,28		14	28	27256
U3	1,28		14	28	27256
U4	7	4		8	LM351
U5	7	4		8	LM351
U6	3	12		14	LM339
U7	3	7	12	5	AD555A
UB	18		3	2	AD754B
U9	8	4		8	LF412A
U10	13	4		16	D6308
U11		8	4		LF412A

| PB | PC | MODE | S1 | S2 | S3 | S4 | S5 | operation:

PB<6>	PB<5>	PB<4>	Waveform selected:
1	0	0	Sine Wave
1	1	0	Triangular Wave
1	0	1	Square Wave
0	0	1	Gaussian noise
0	1	0	Smooth Wave
0	0	0	4 step Staircase
1	0	0	option wave
1	1	0	option wave

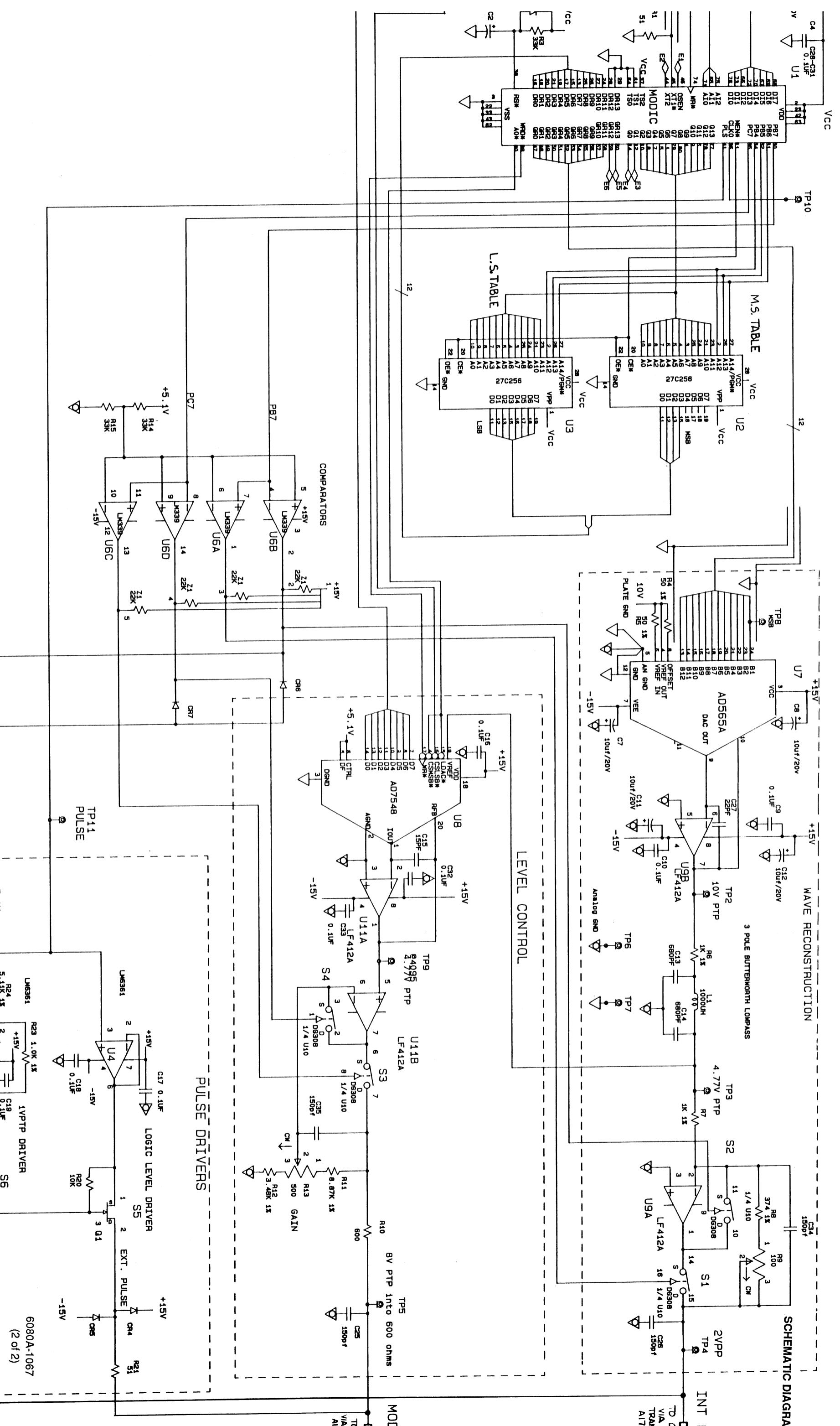
NOTE: (unless otherwise specified)

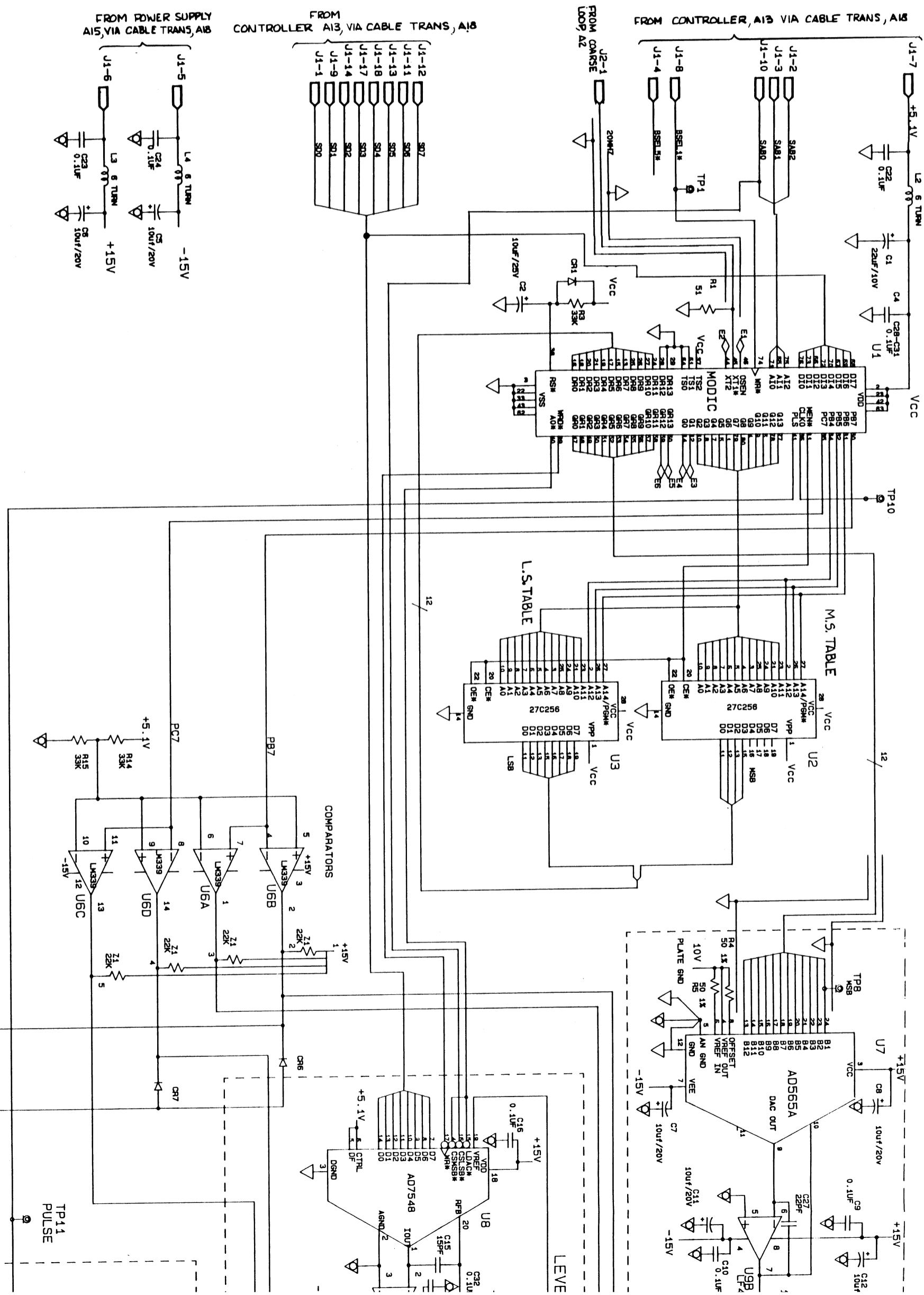
- 1) All capacitor values are in uF.
- 2) All resistor values are in Ohms.

Sn- refers to switches on the PCB.

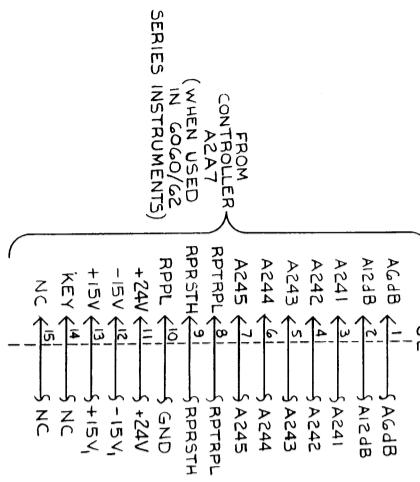
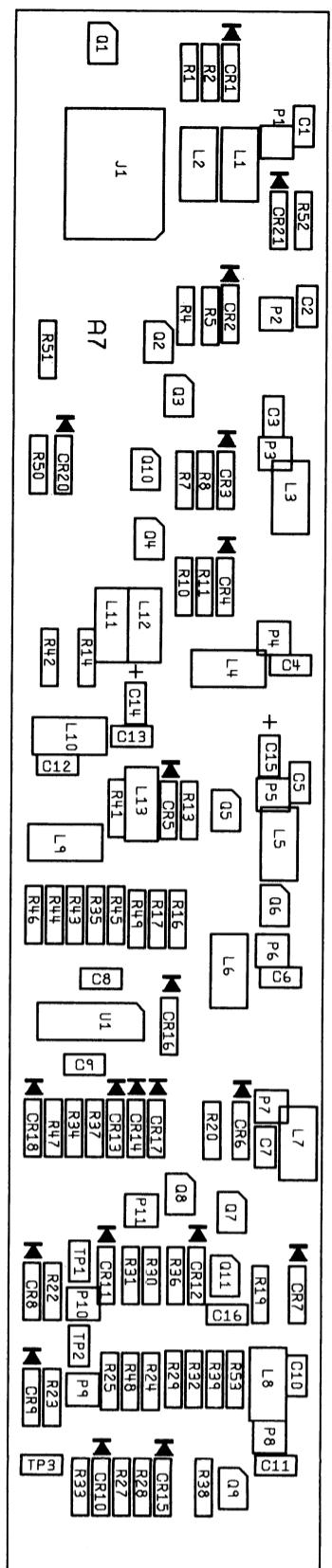
6080A-1067
(1 of 2)

Figure 8-6. A6 Mod Oscillator PCA





SCHEMATIC DIAGRAMS



TO
A₇
ATTENUATOR/
ATTEN.

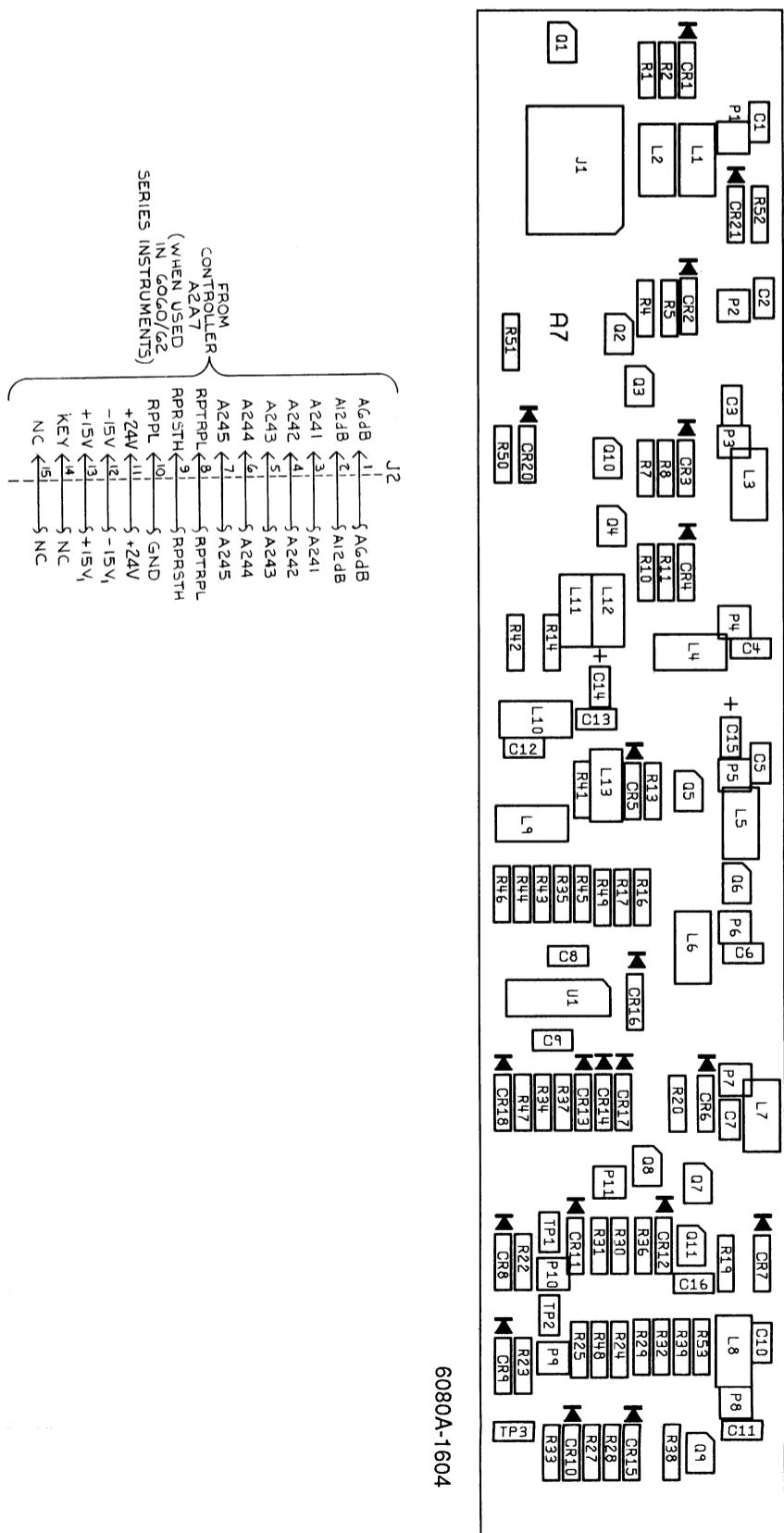
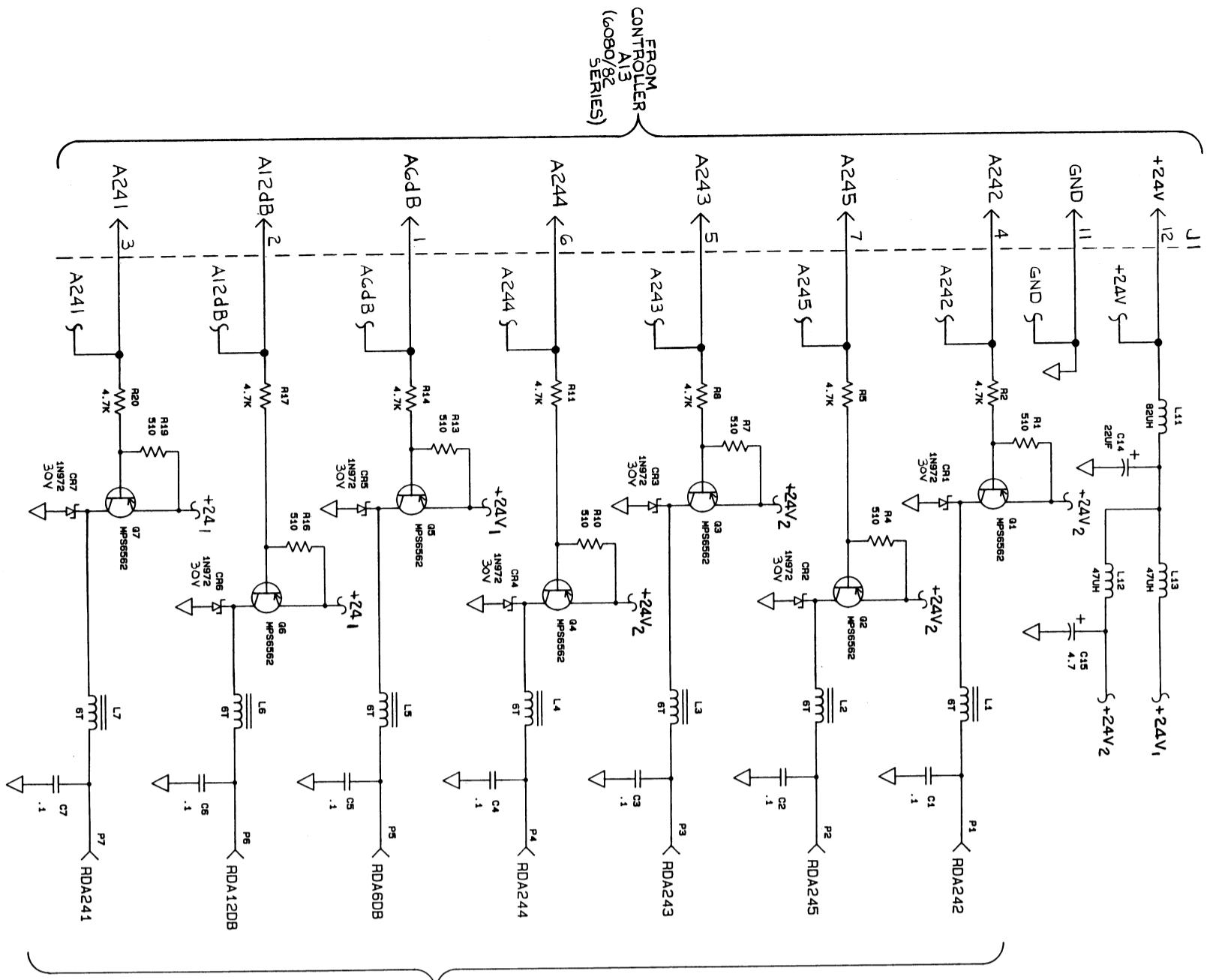
NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL RESISTORS ARE 1/4W, 5%.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE	DESIGNATIONS
U1	NOT USED

6080A-1032
(1 of 2)

Figure 8-7. A7 Relay Driver PCA



NOTES: (UNLESS OTHERWISE SPECIFIED)

- (UNLESS OTHERWISE SPECIFIED)

6080A-1032
(1 of 2)

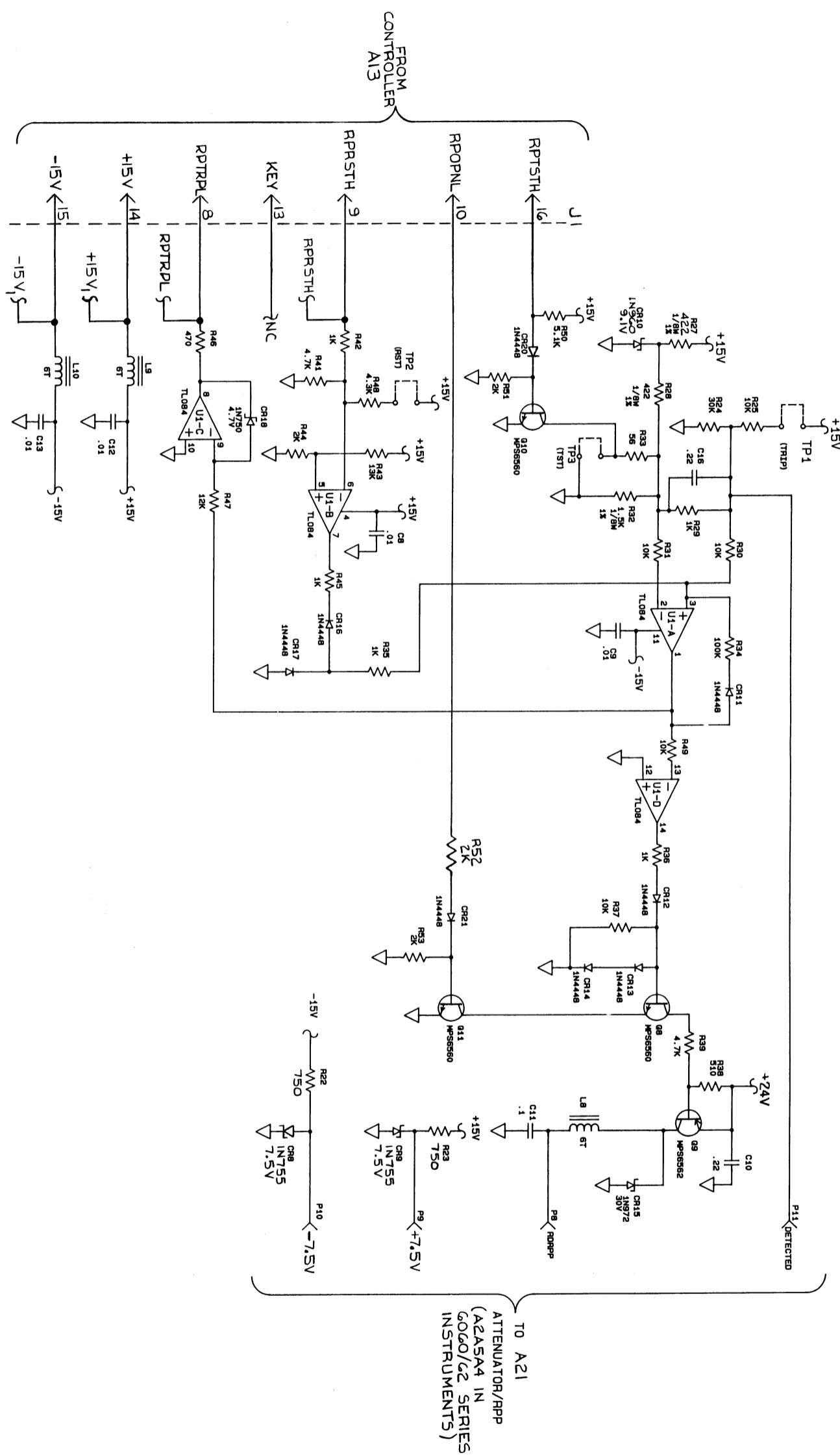
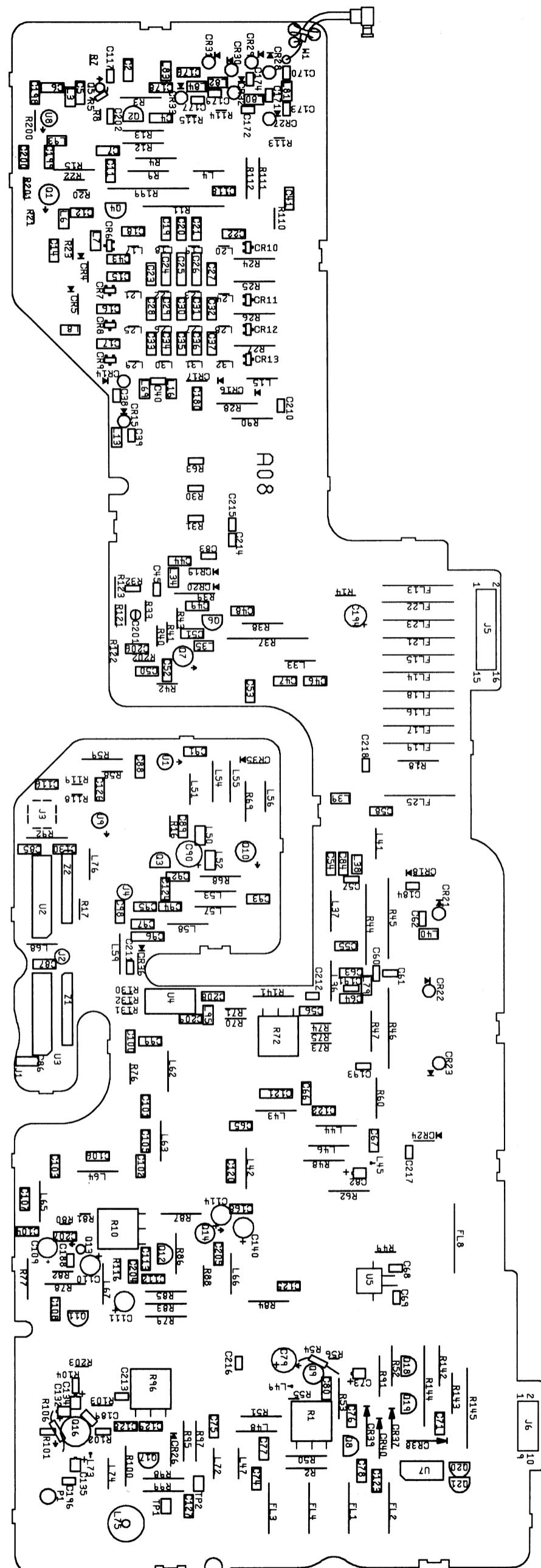


Figure 8-7. A7 Relay Driver PCA (cont)

SCHEMATIC DIAGRAMS



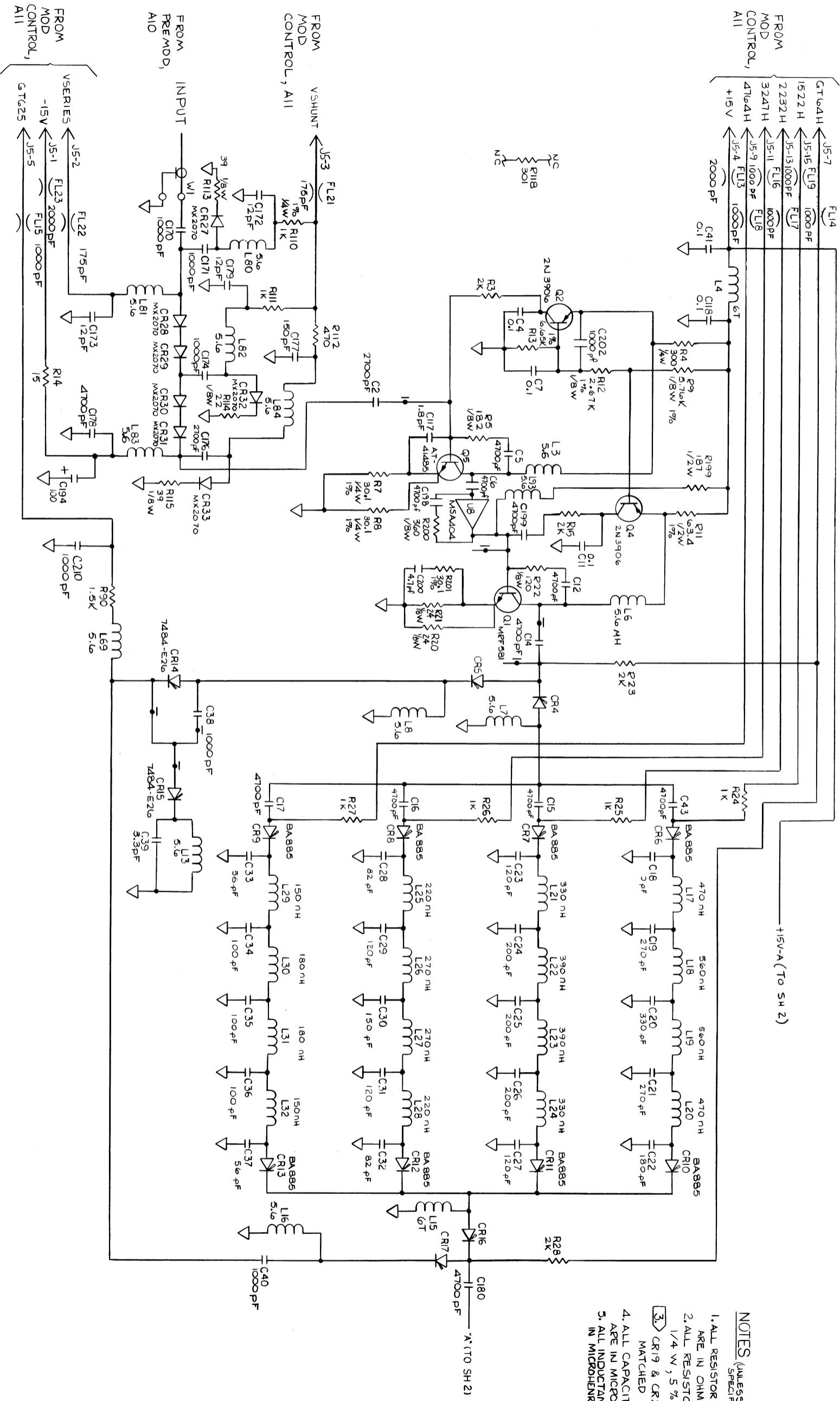


Figure 8-8. A8 Output PCA (cont)

6080A-1040
(1 of 3)

SCHEMATIC DIAGRAMS

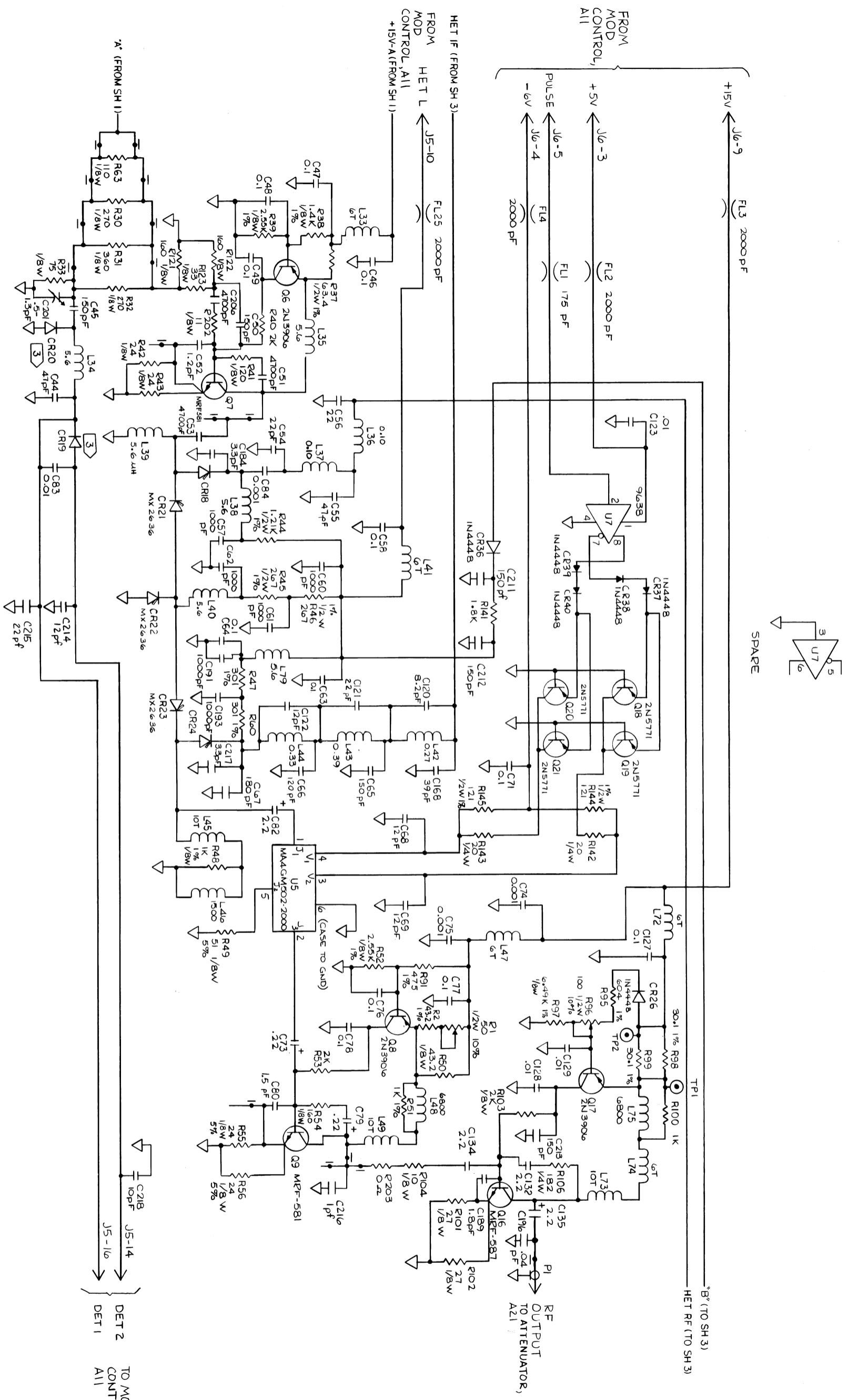
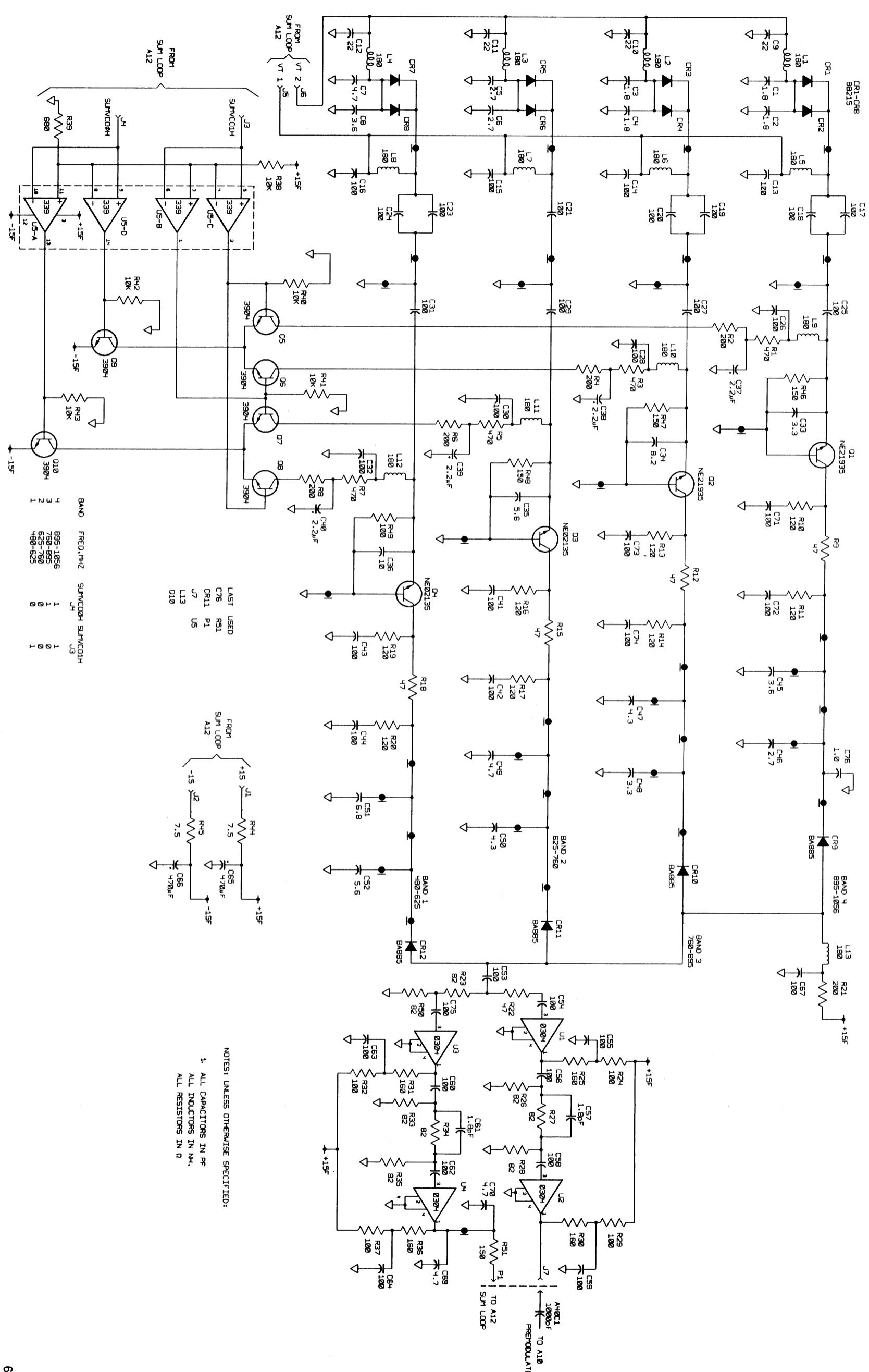


Figure 8-8. A8 Output PCA (cont)

Figure 8-9. A9 Sum Loop VCO PCA (cont)

6080A-1041



Schematic Diagrams

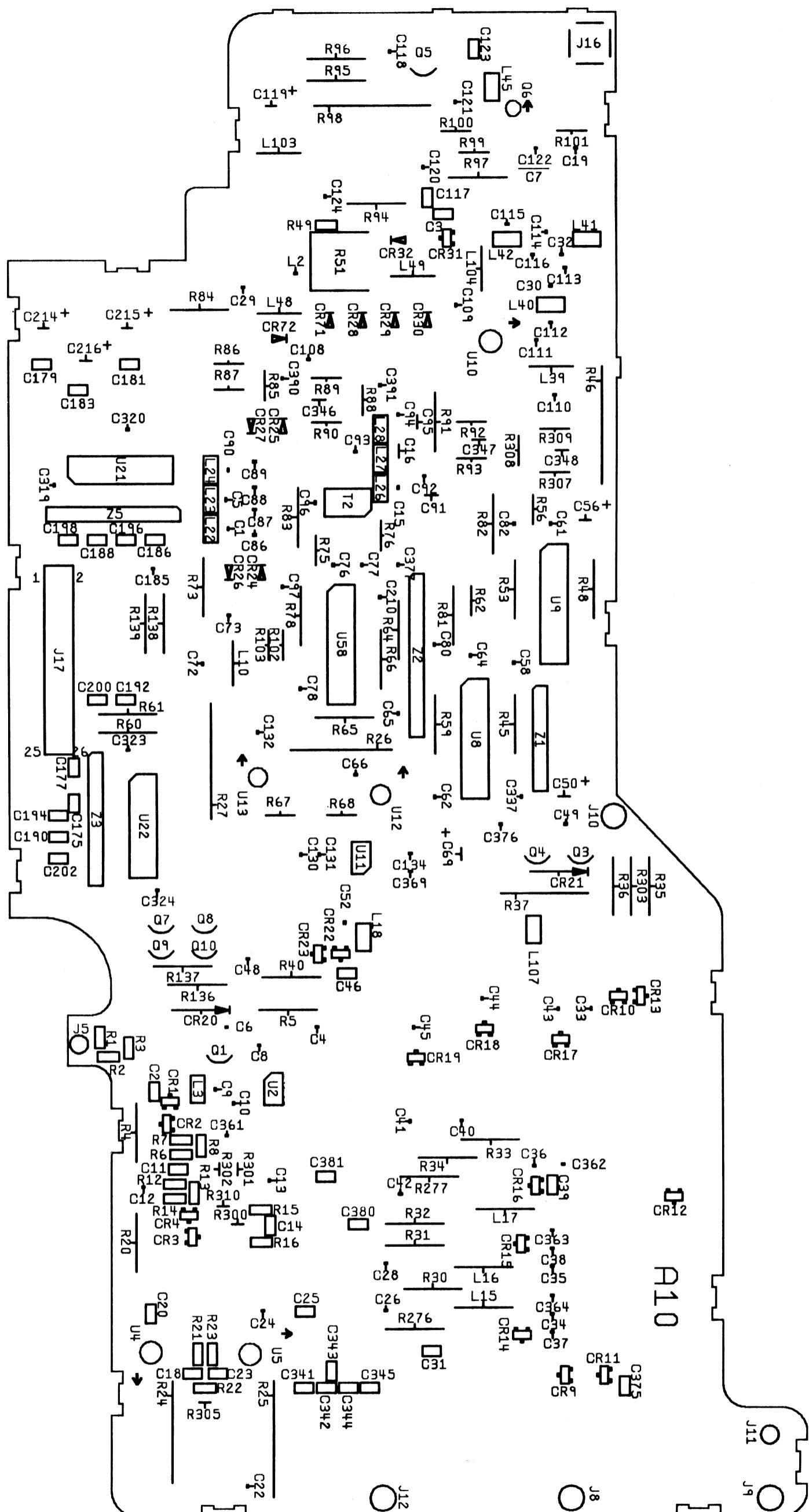


Figure 8-10. A10 Premodulator PCA

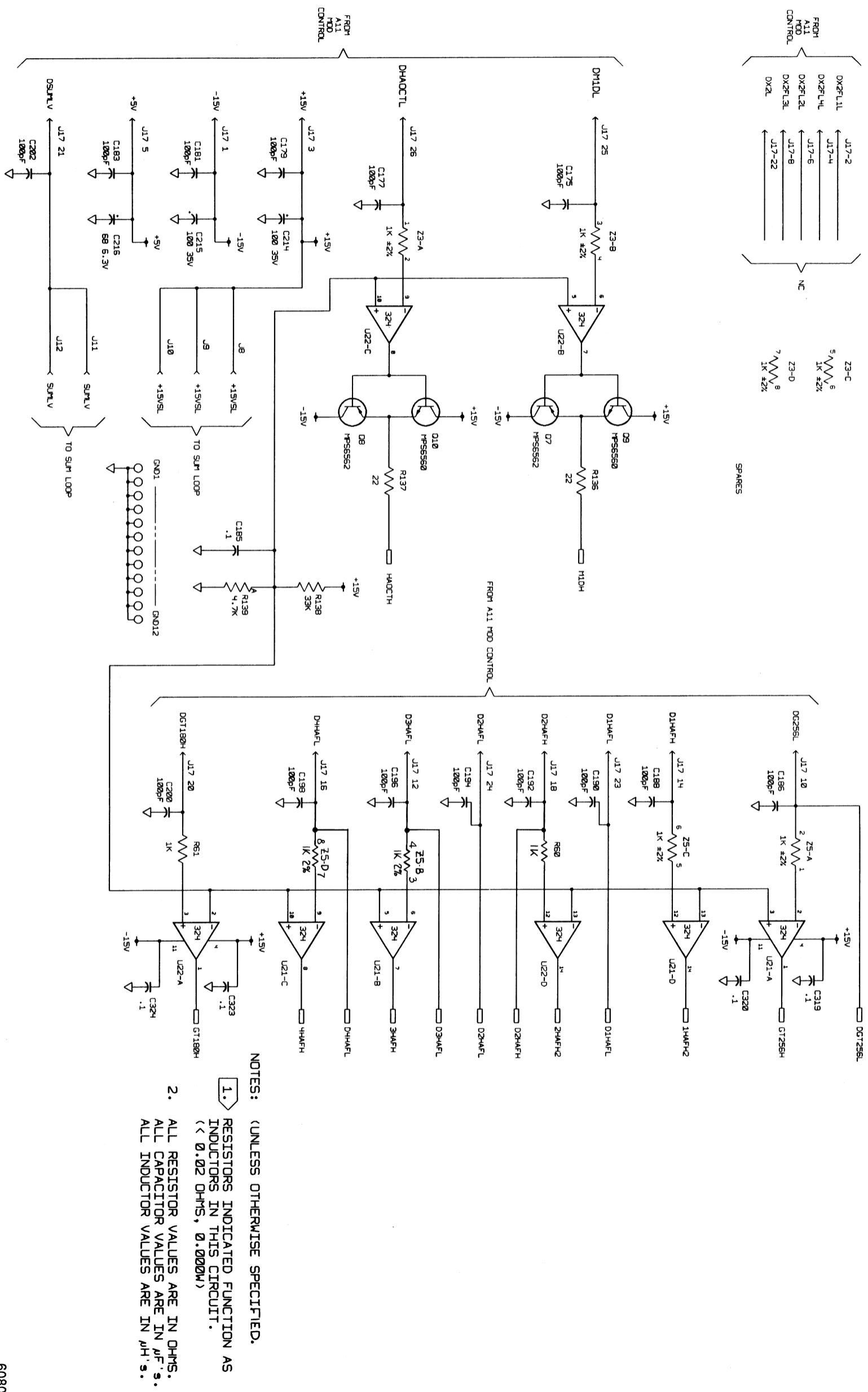


Figure 8-10. A10 Premodulator PCA (cont)

SCHEMATIC DIAGRAMS

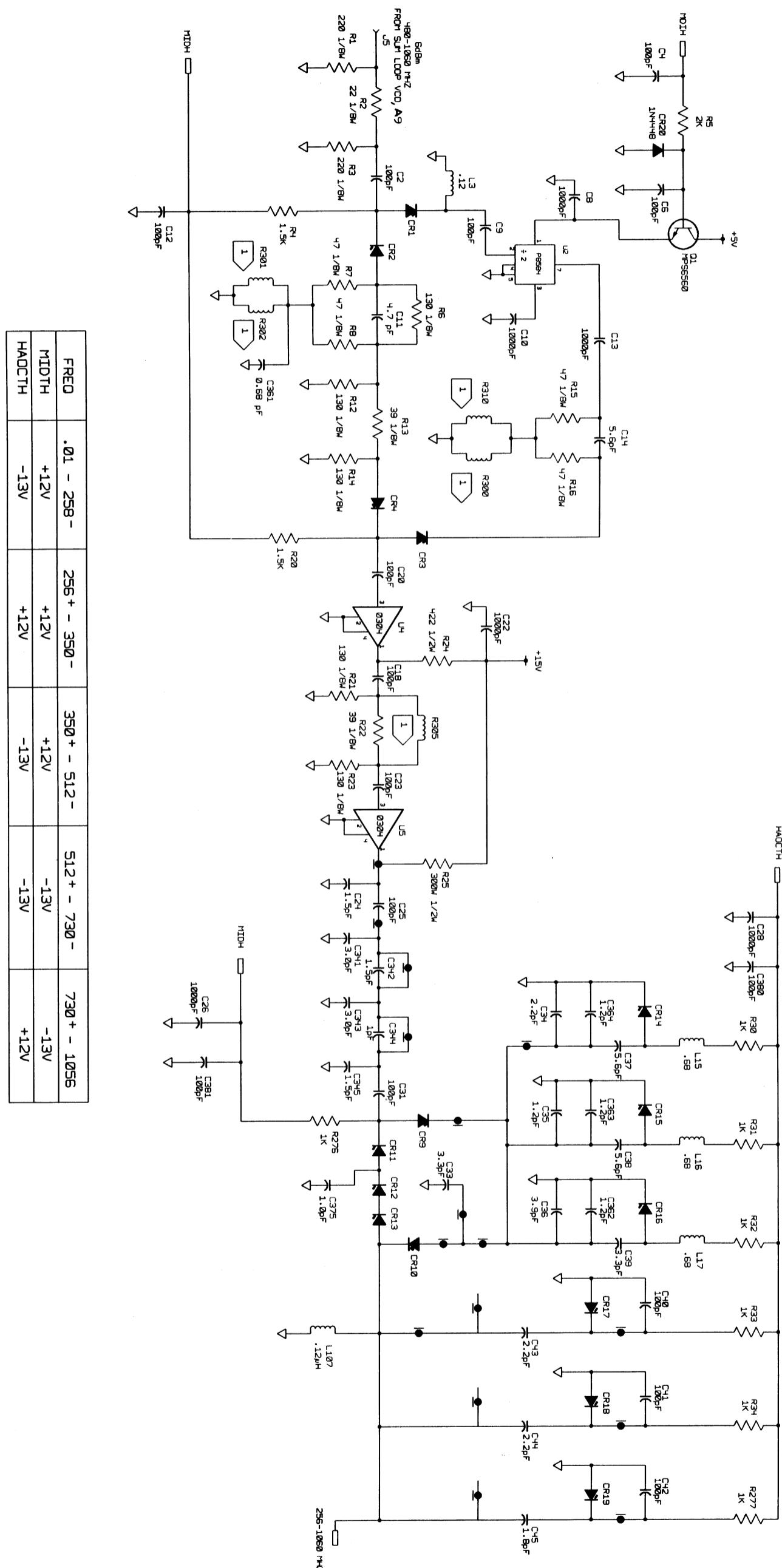


Figure 8-10. A10 Premodulator PCA (cont)

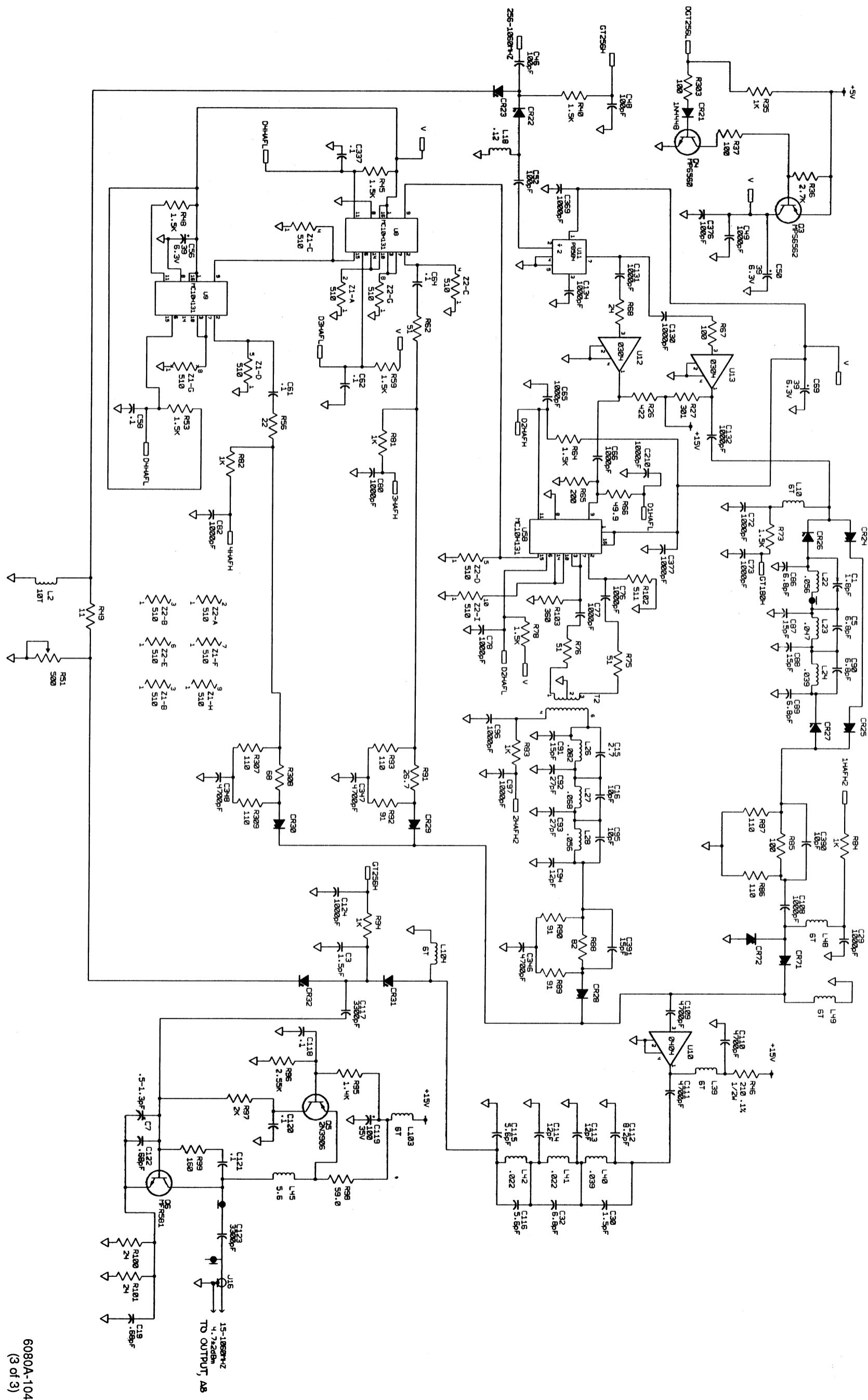


Figure 8-10. A10 Premodulator PCA (cont)

6080A-1046
(3 of 3)

SCHEMATIC DIAGRAMS

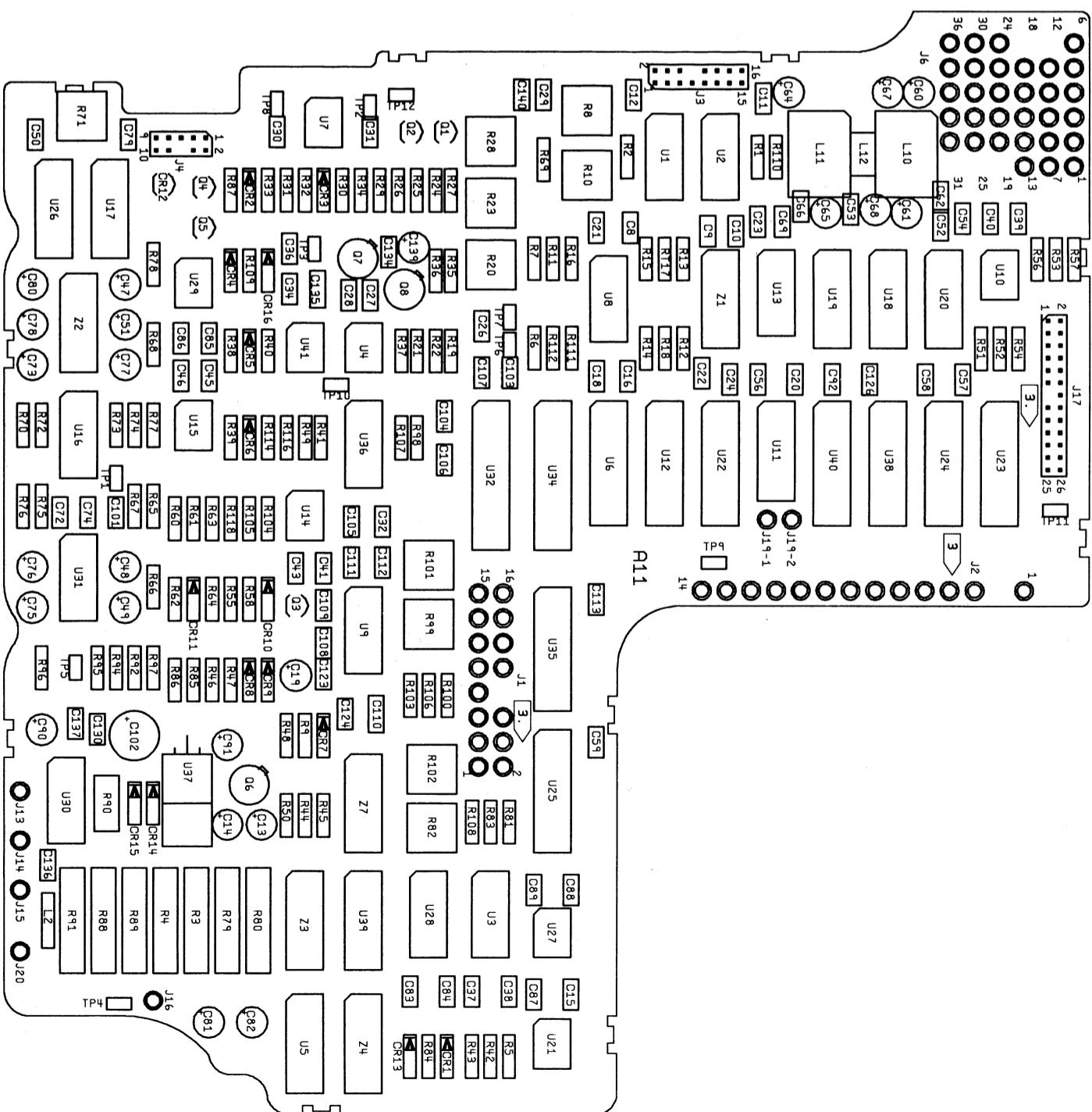


Figure 8-11. A11 Modulation Control PCA

SCHEMATIC DIAGRAMS

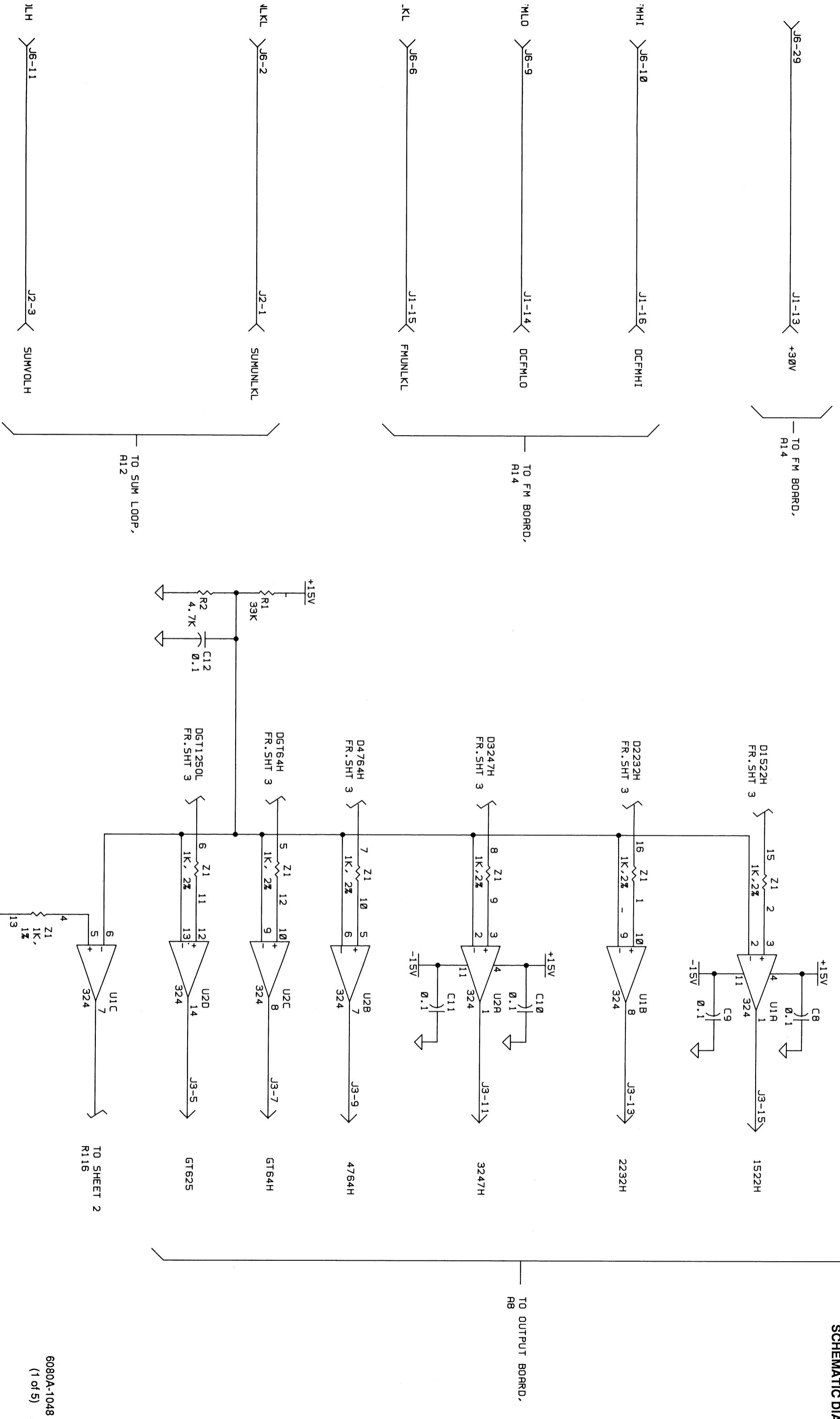
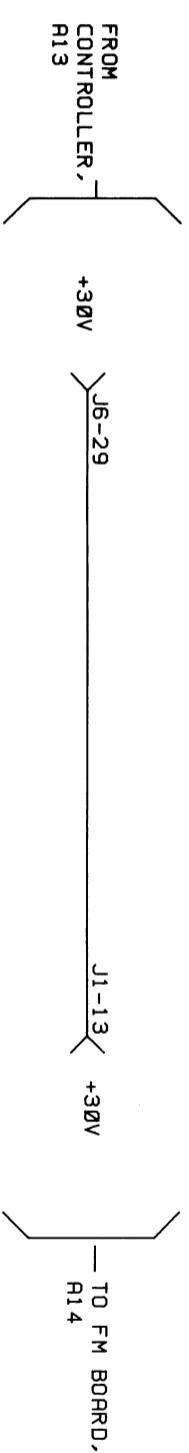


Figure 8-11. A11 Modulation Control PCA (cont)

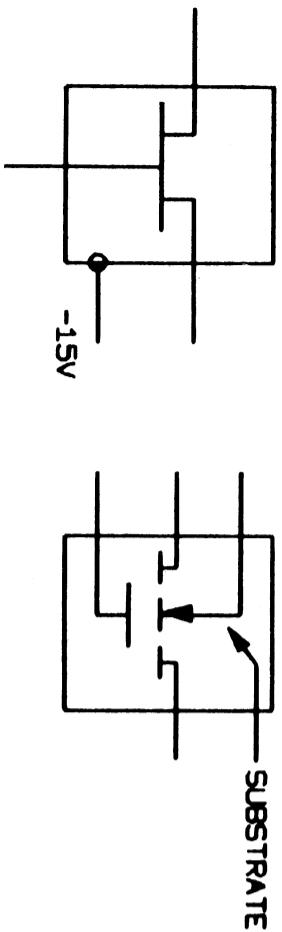
6080A-1048
(1 of 5)

NOTE:
DS5002'S WHICH
ARE SHOWN AS:

ARE ACTUALLY
CONSTRUCTED:



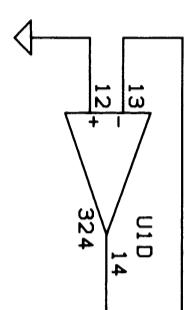
D1522H
FR.SHT 3
— 15 Z
1K, 2



TO CONTROLLER, — R13 — DCFMHI — J6-10 — J1-16 — DCFMHI — A13 — TO FM BOARD,

DCFMLO — J6-9 — J1-14 — DCFMLO — R14 — TO FM BOARD,

D3247H
FR.SHT 3
— 8 Z1
1K, 2



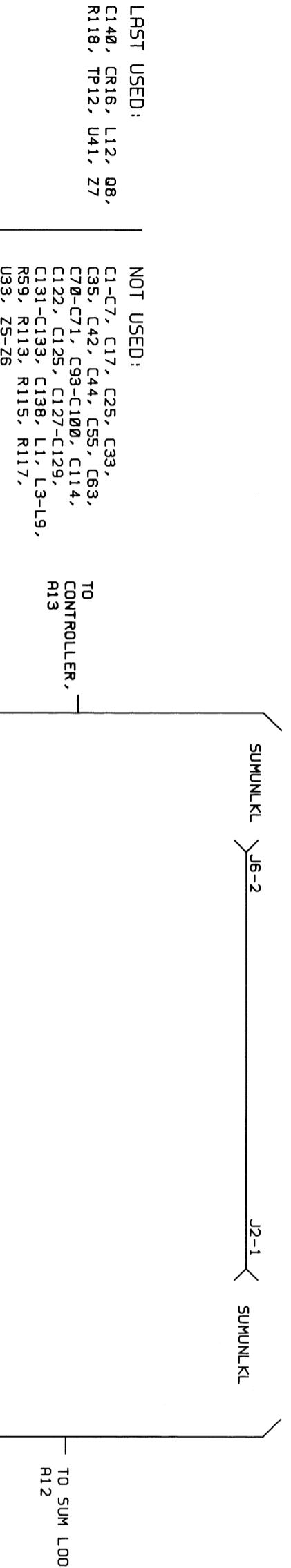
FMUNLKL — J6-6 — J1-15 — FMUNLKL — R13 — TO FM BOARD,

D4764H
FR.SHT 3
— 5 Z1
1K, 2

SUMUNLKL — J6-2 — J2-1 — SUMUNLKL — R13 — TO CONTROLLER,

+15V
R1
33K
R2
4.7K
C12
0.1
DG164H
FR.SHT 3
— 6 Z1
1K, 2

DG1250L
FR.SHT 3
— 7 Z1
1K, 2



SUMVOLH — J6-11 — J2-3 — SUMVOLH — R12 — TO SUM LOOP,

Schematic Diagrams

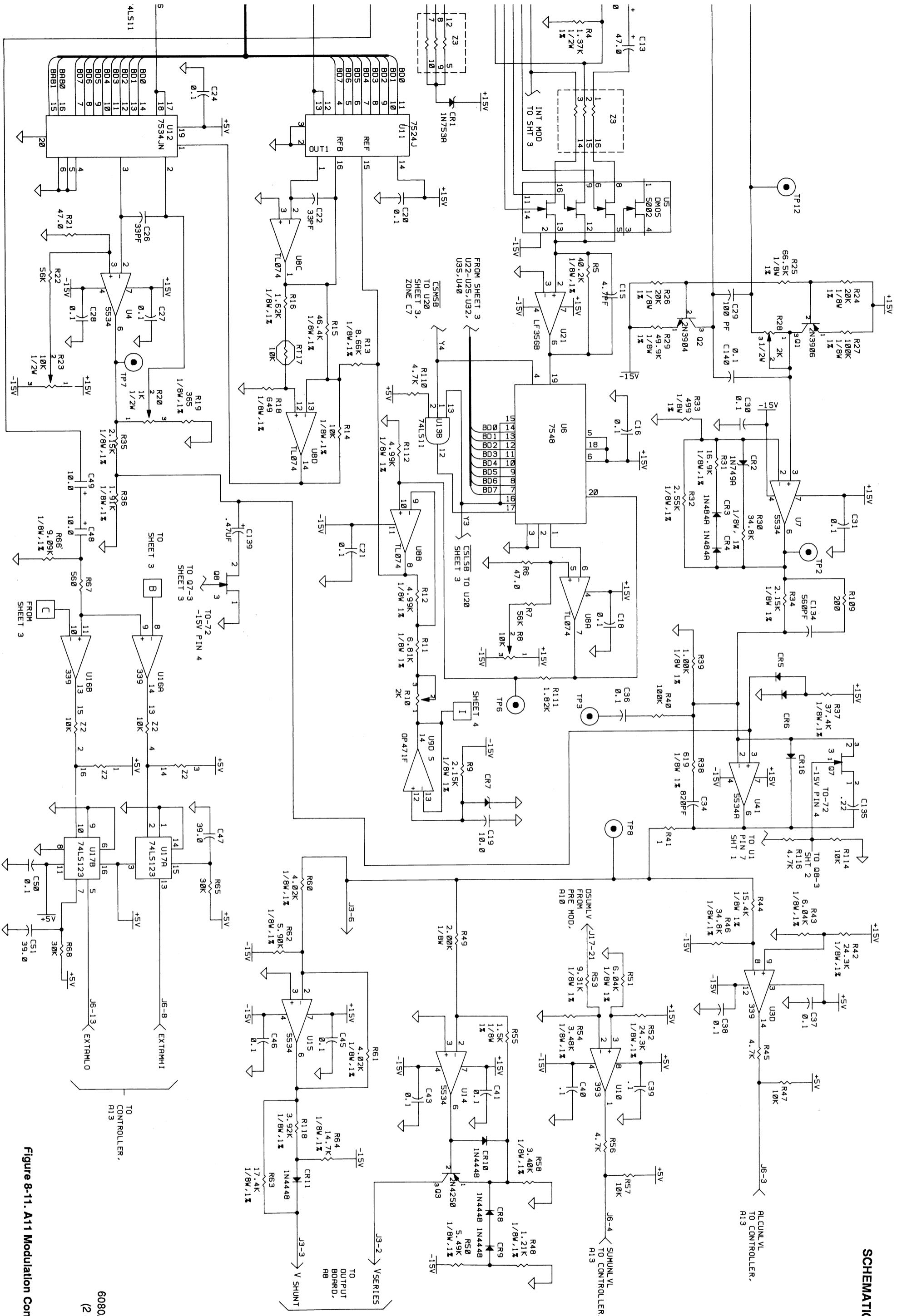
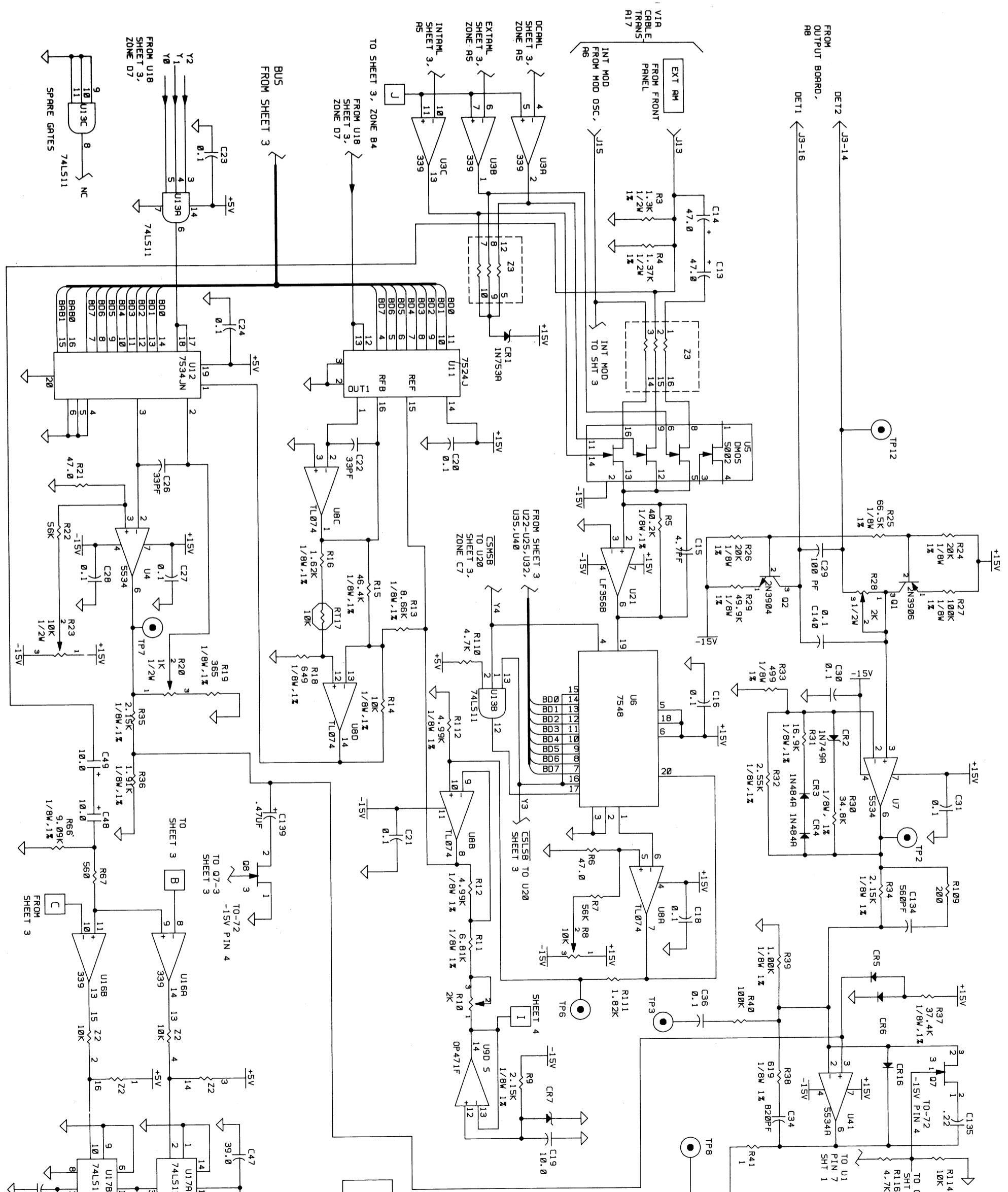
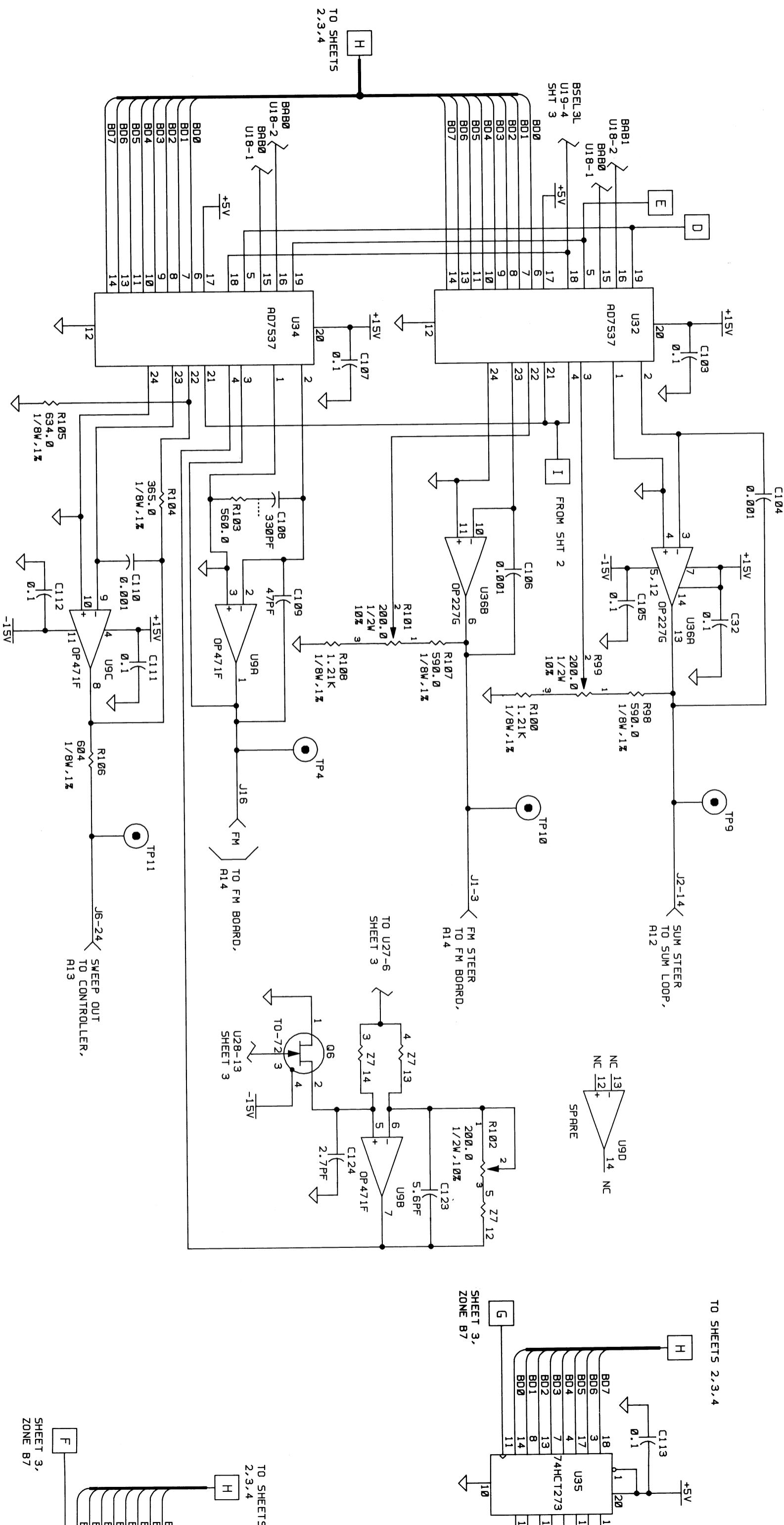


Figure 8-11. A11 Modulation Control PCA (cont)





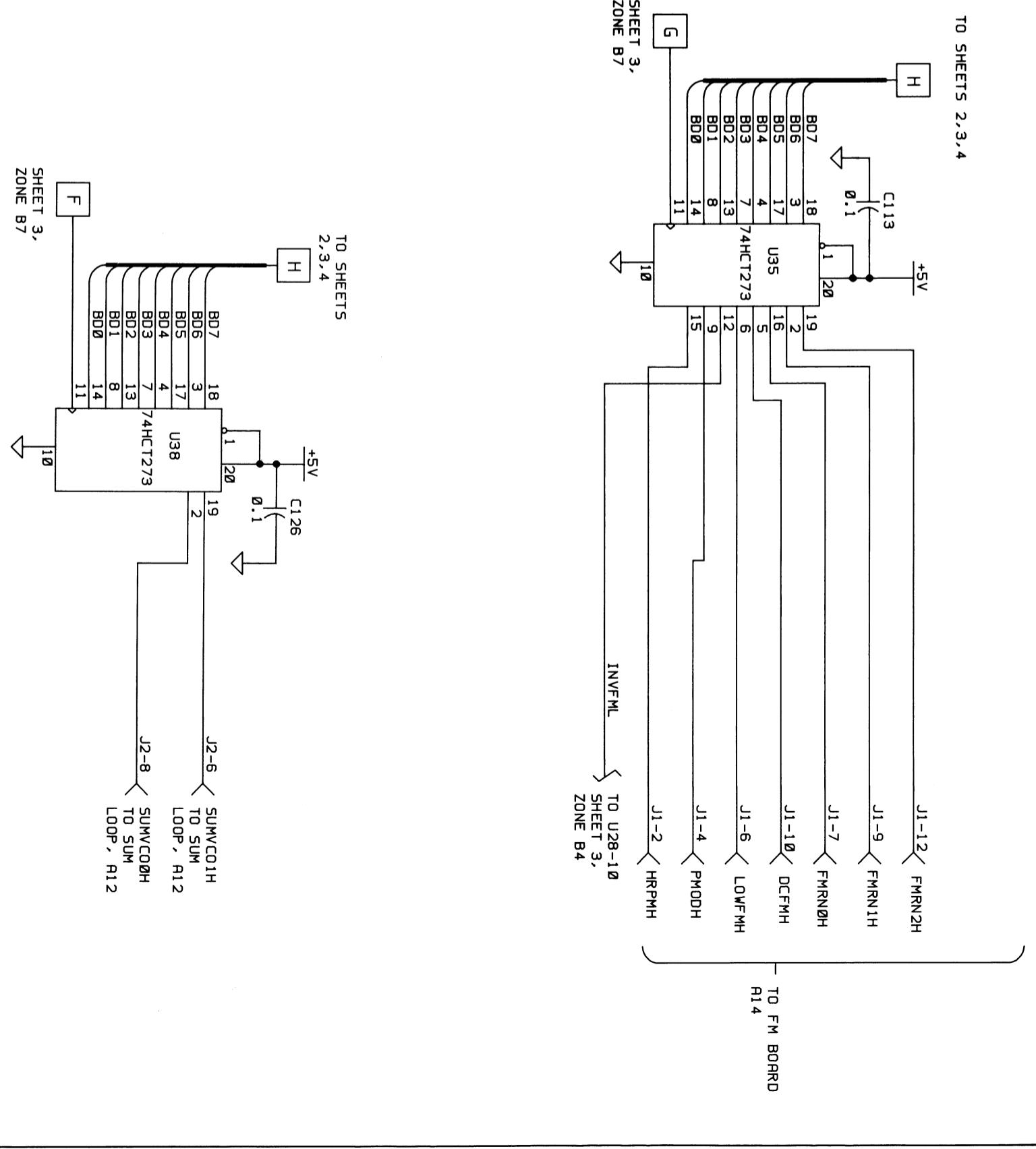
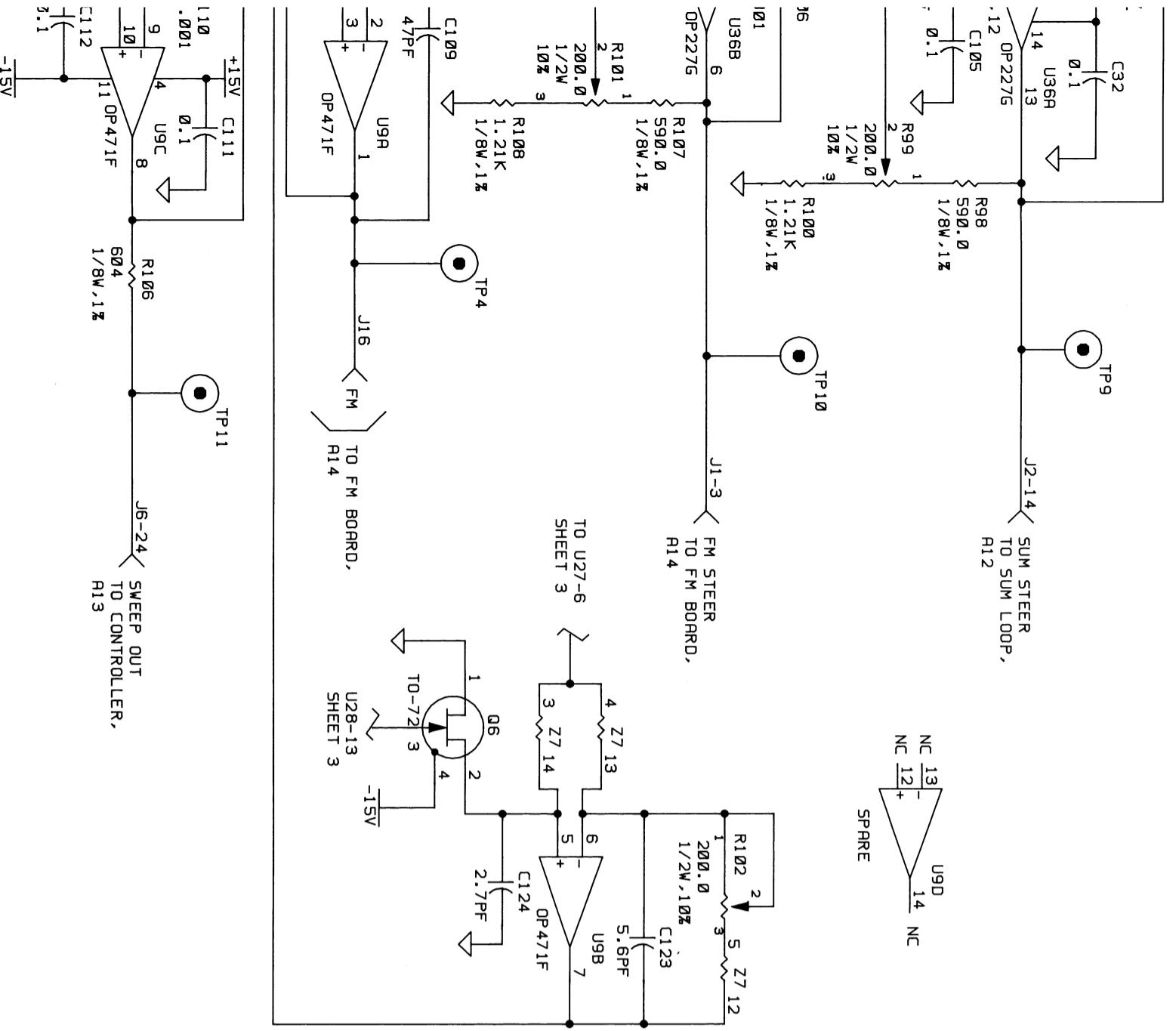
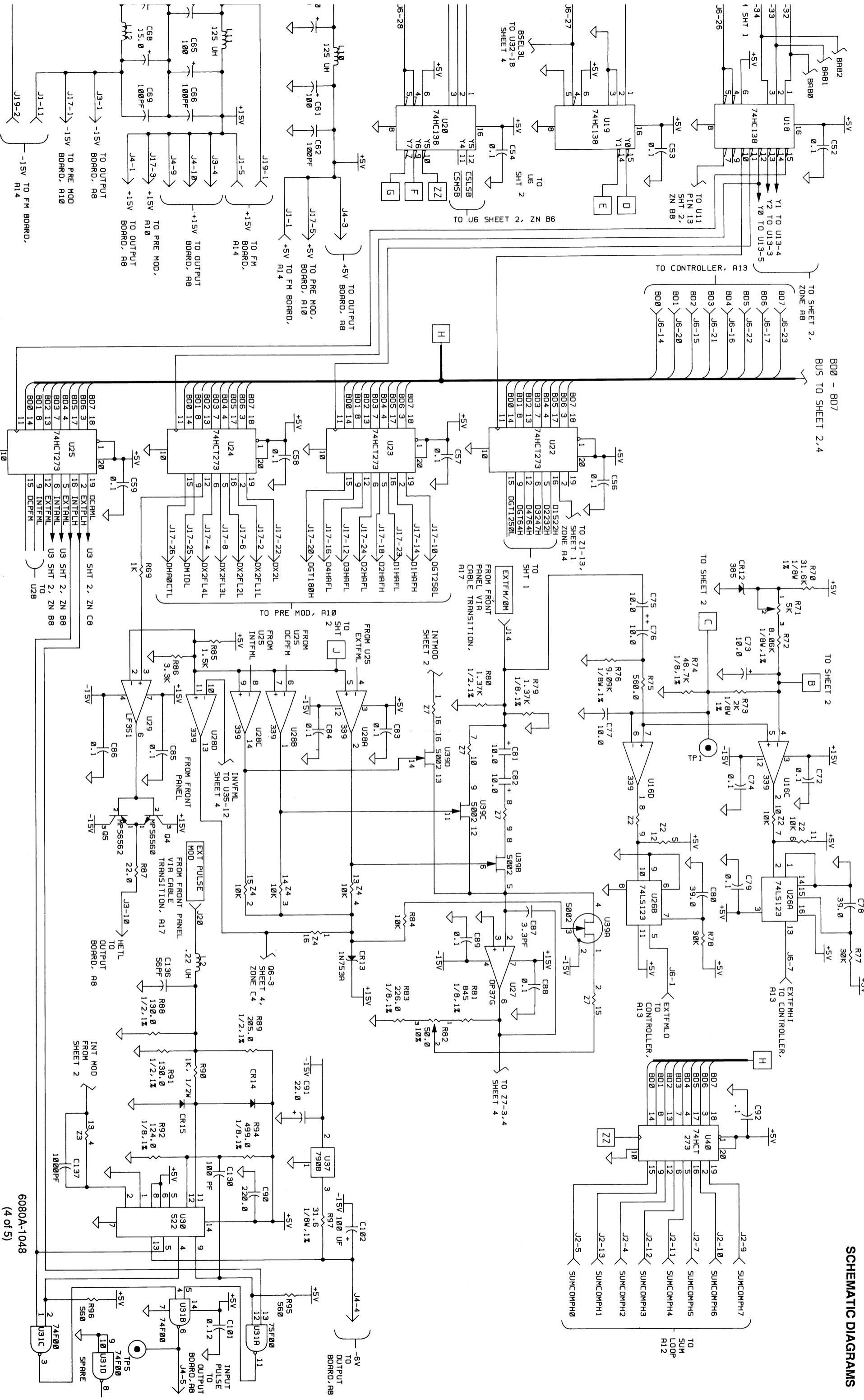


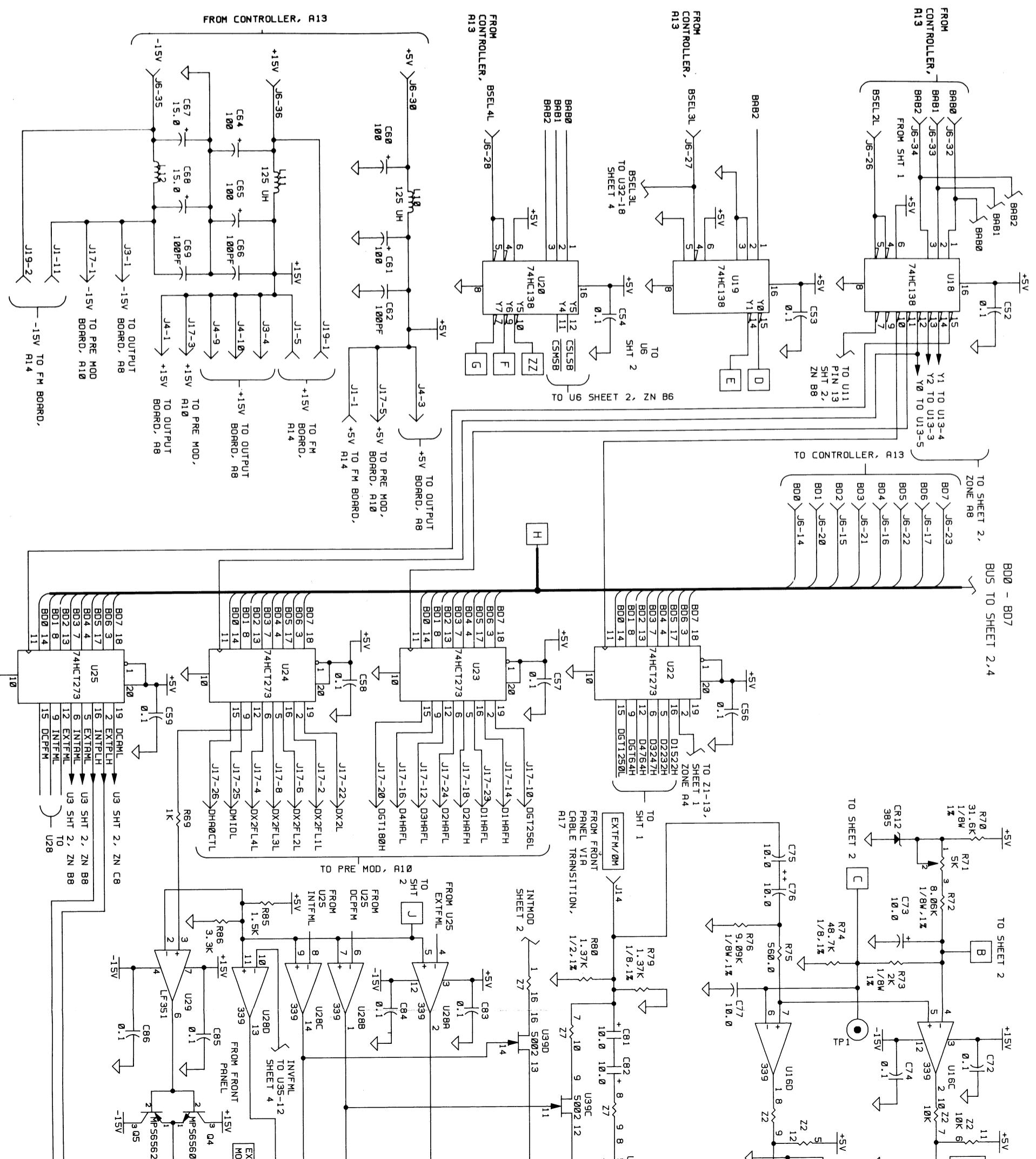
Figure 8-11. A11 Modulation Control PCA (cont)

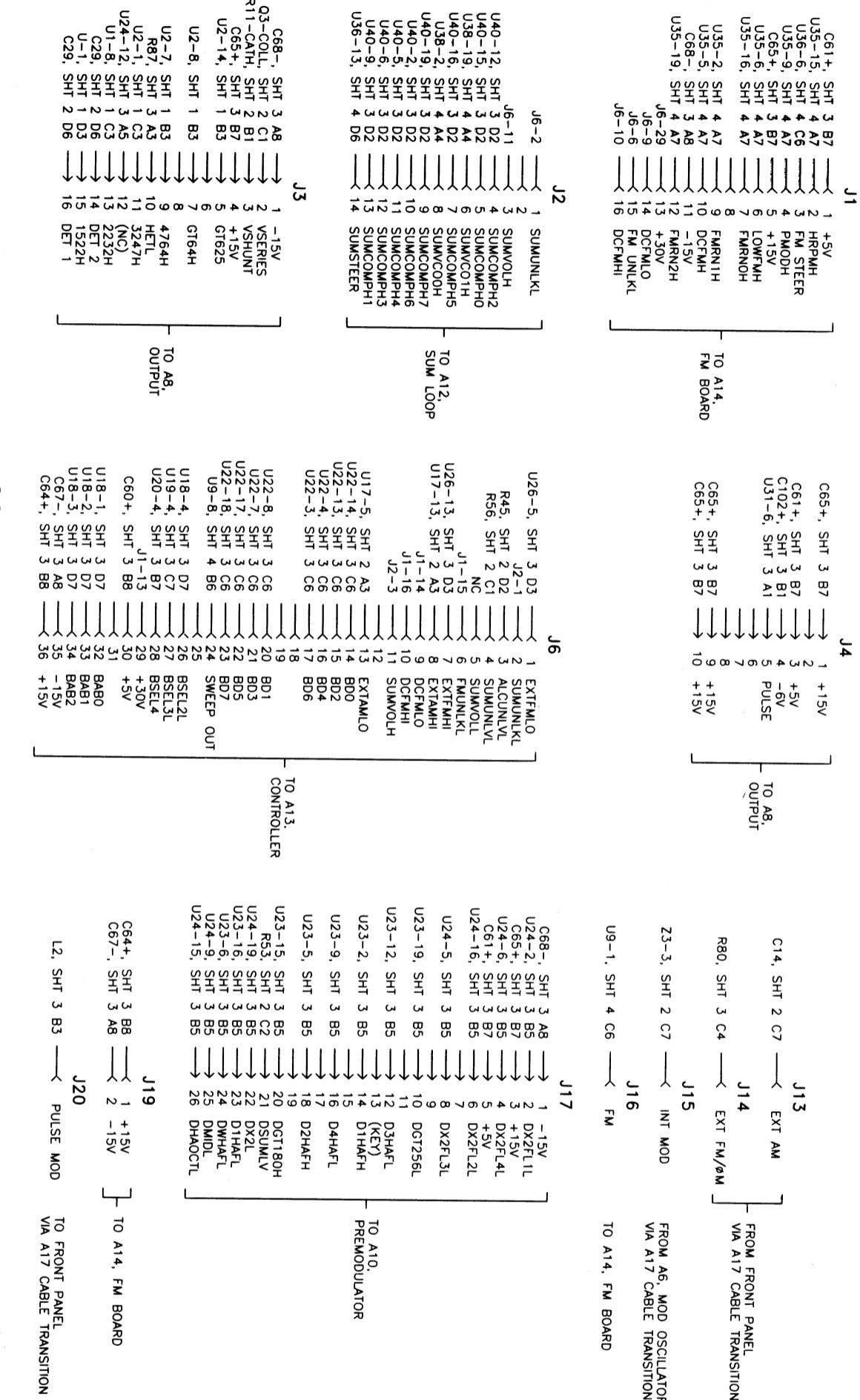
6080A-1048
(3 of 5)

SCHEMATIC DIAGRAMS

Figure 8-11. A11 Modulation Control PCA (cont)







CONNECTOR SUMMARY

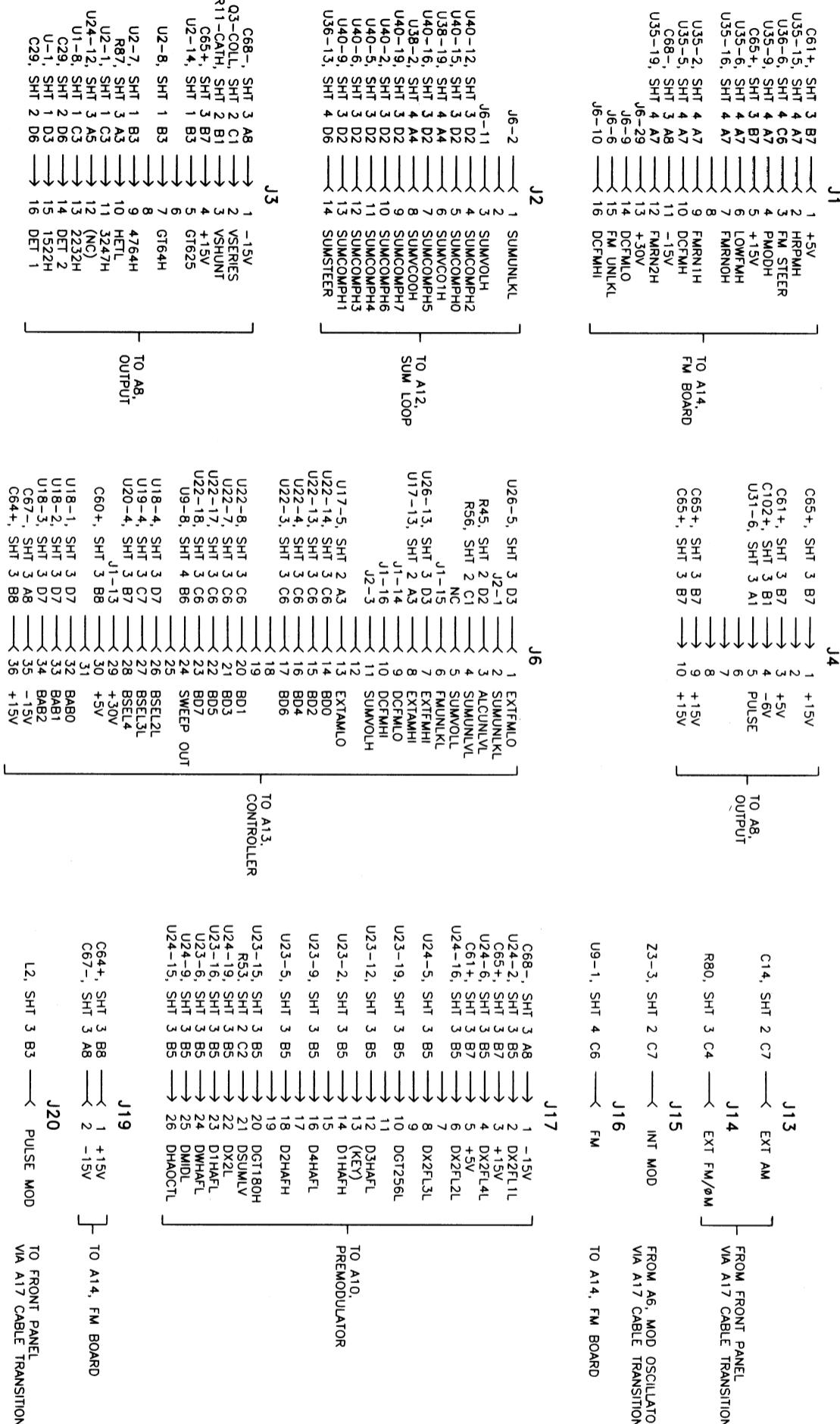


Figure 8-11. A11 Modulation Control PCA (cont)

Schematic Diagrams

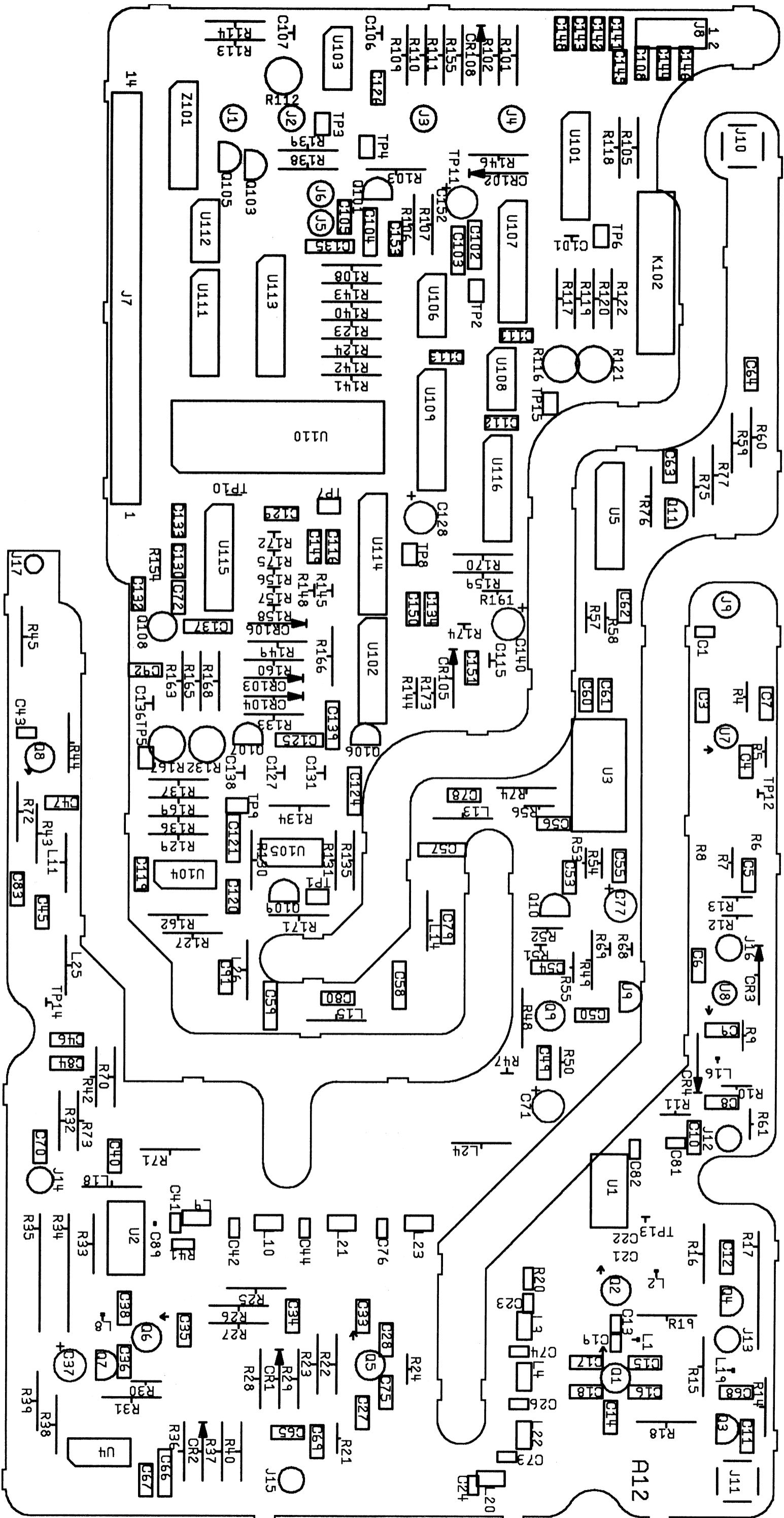


Figure 8-12. A12 Sum Loop PCA

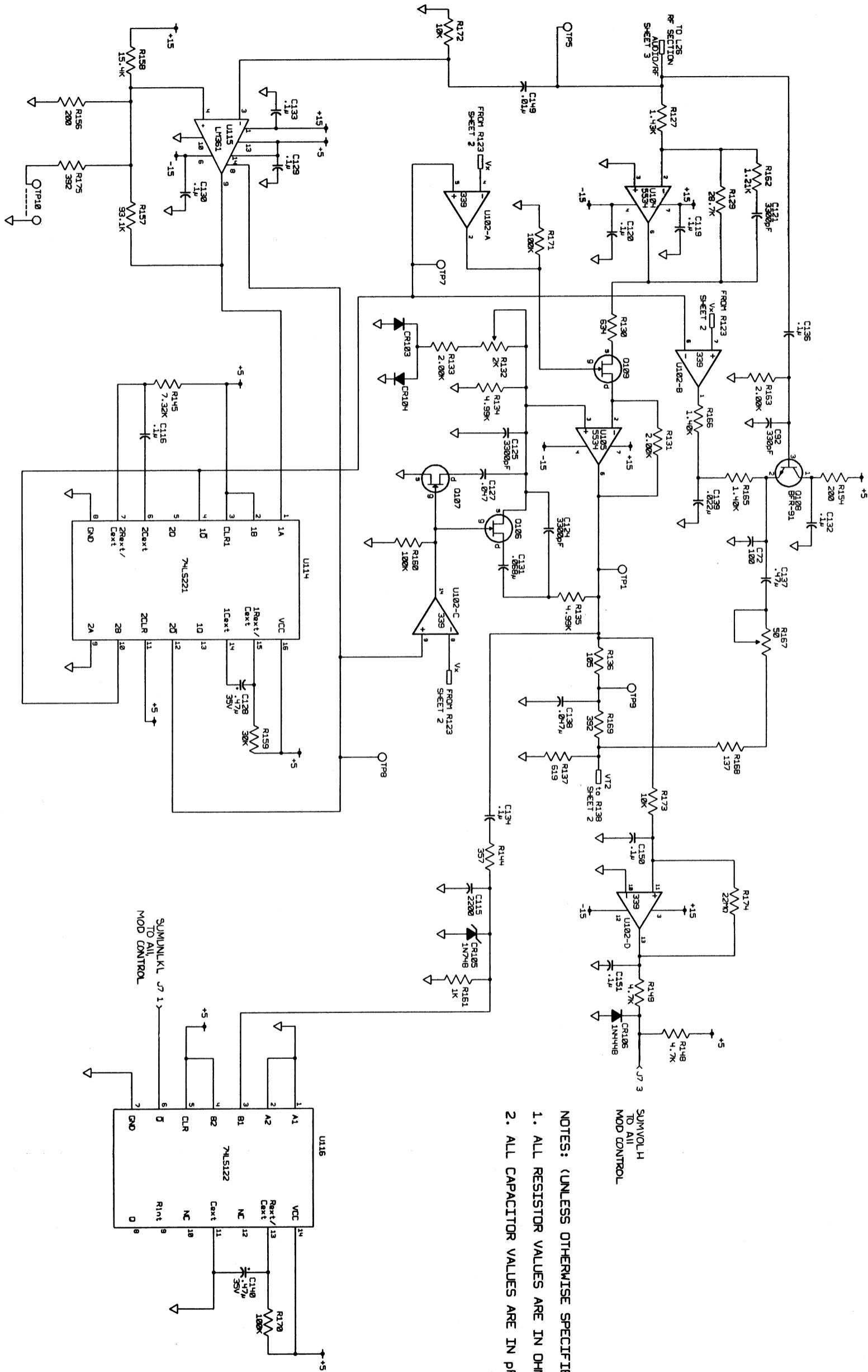


Figure 8-12. A12 Sum Loop PCA (cont.)

6080A-1042
(1 of 3)

Schematic Diagrams

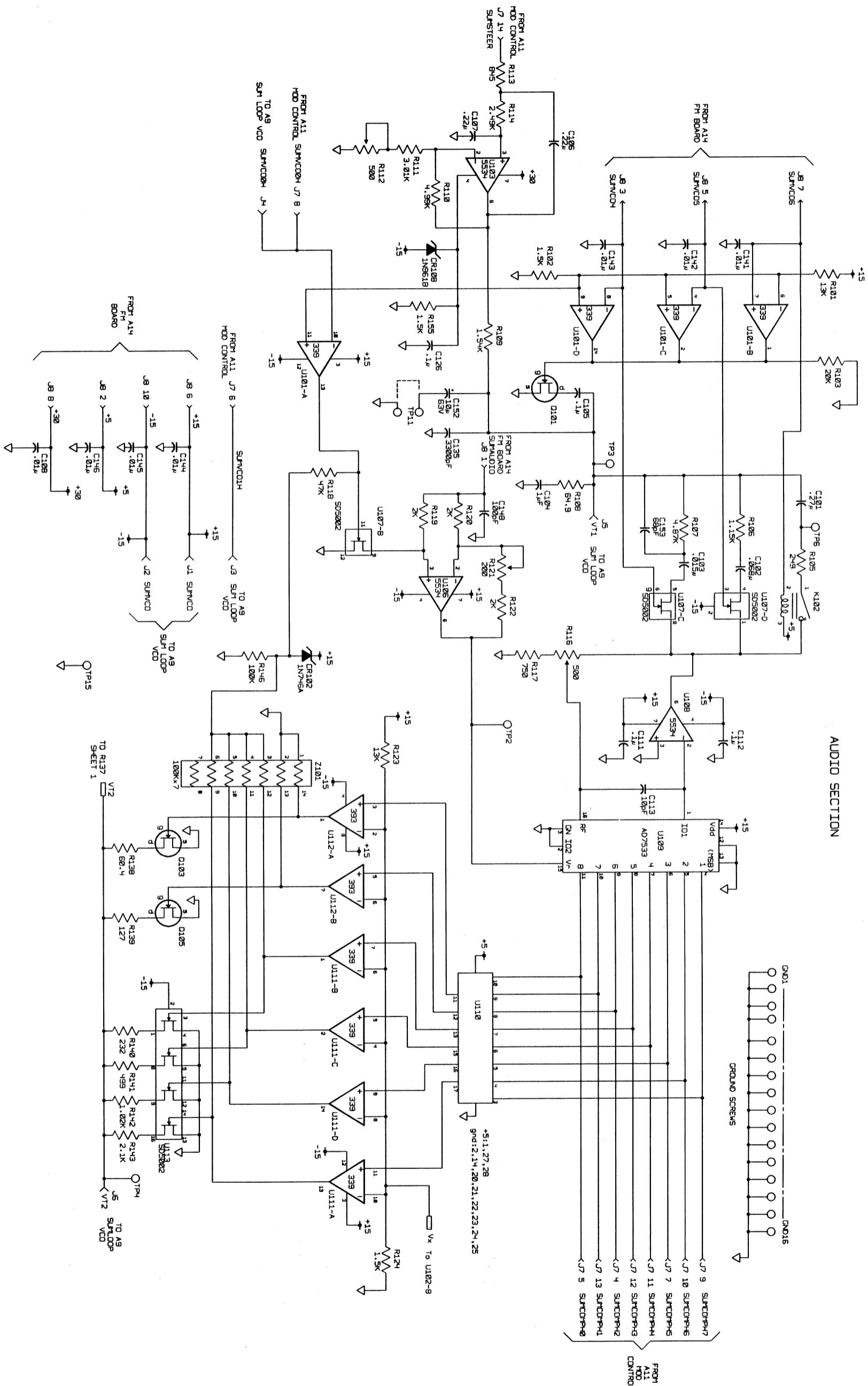


Figure 8-12. A12 Sum Loop PCA (cont)

RF SECTION

+15V PRE-AMP
J13
J14
J15

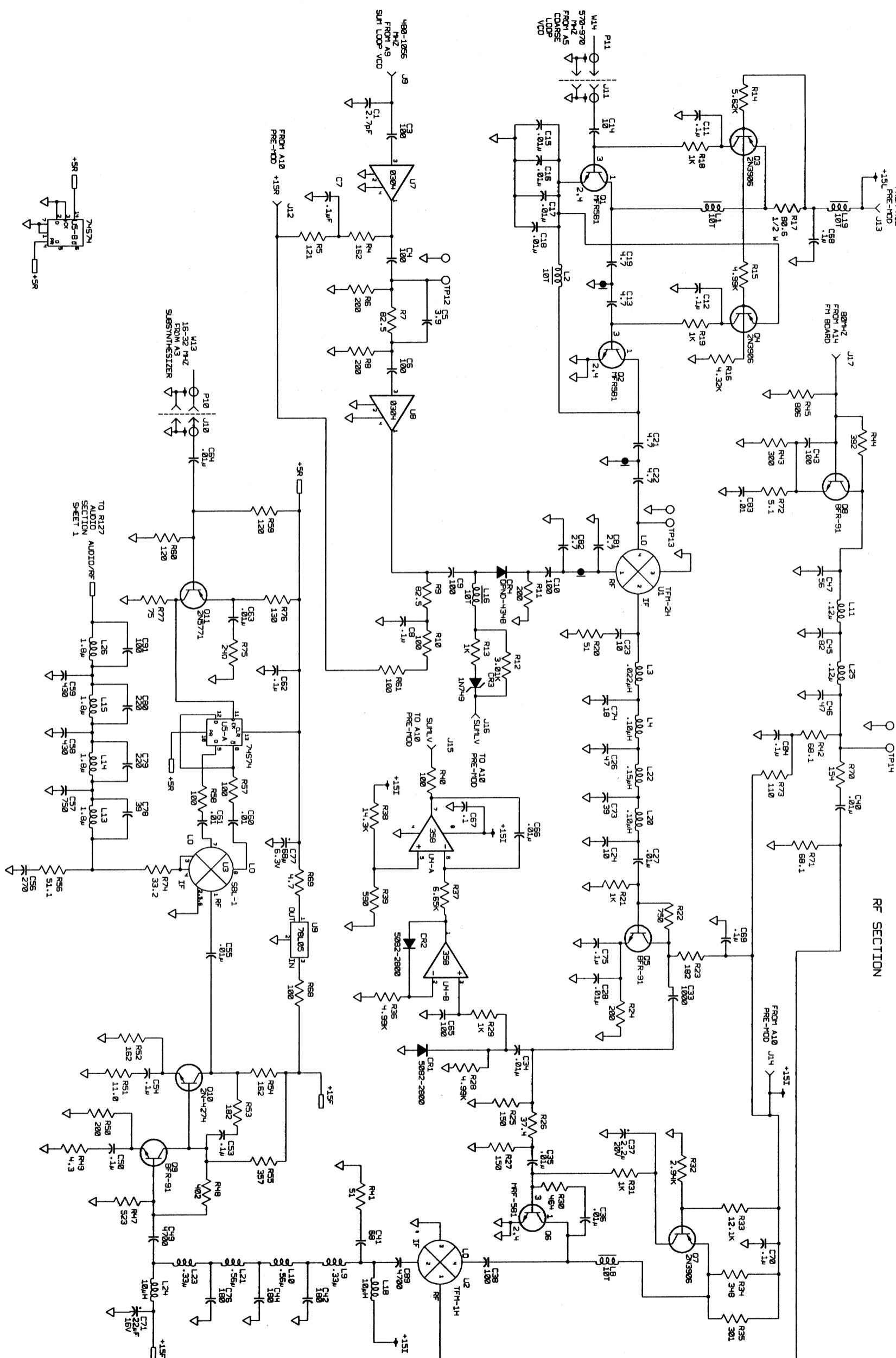
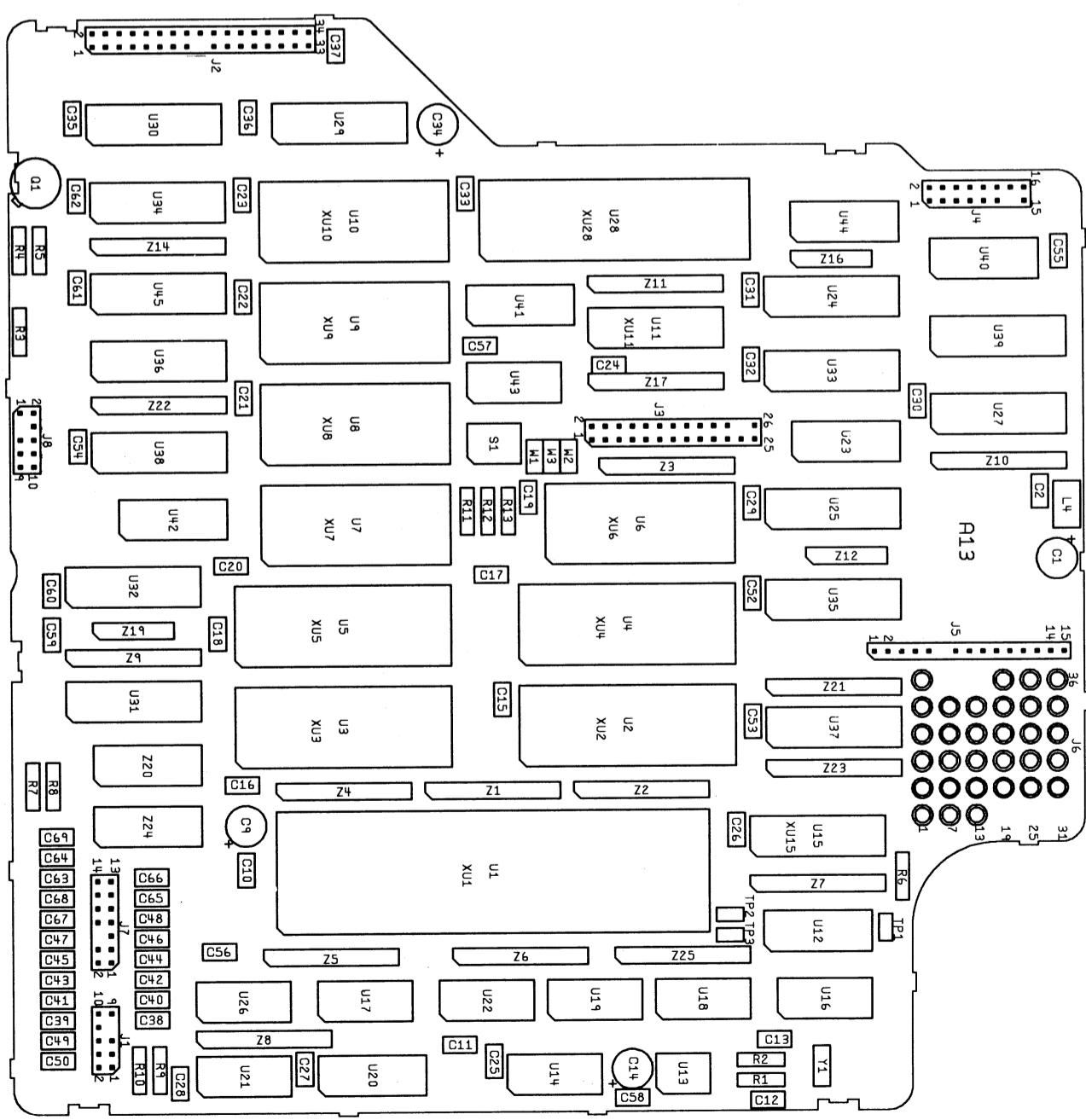


Figure 8-12. A12 Sum Loop PCA (cont)

6080A-104,
(3 of 3)

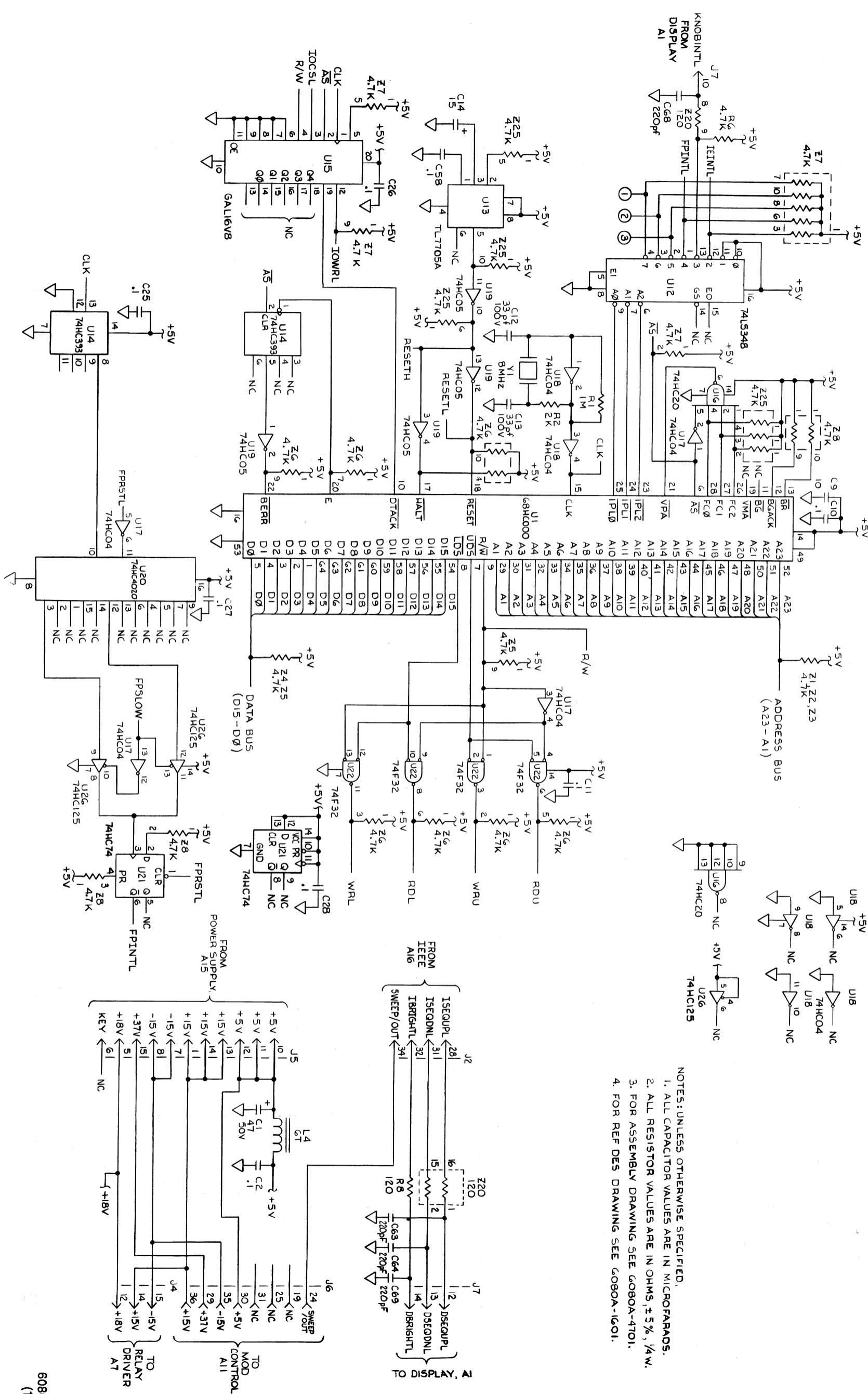
SCHEMATIC DIAGRAMS



Schematic Diagrams

Figure 8-13. A13 Controller PCA (cont)

6080A-1043
(1 of 4)



SCHEMATIC DIAGRAMS

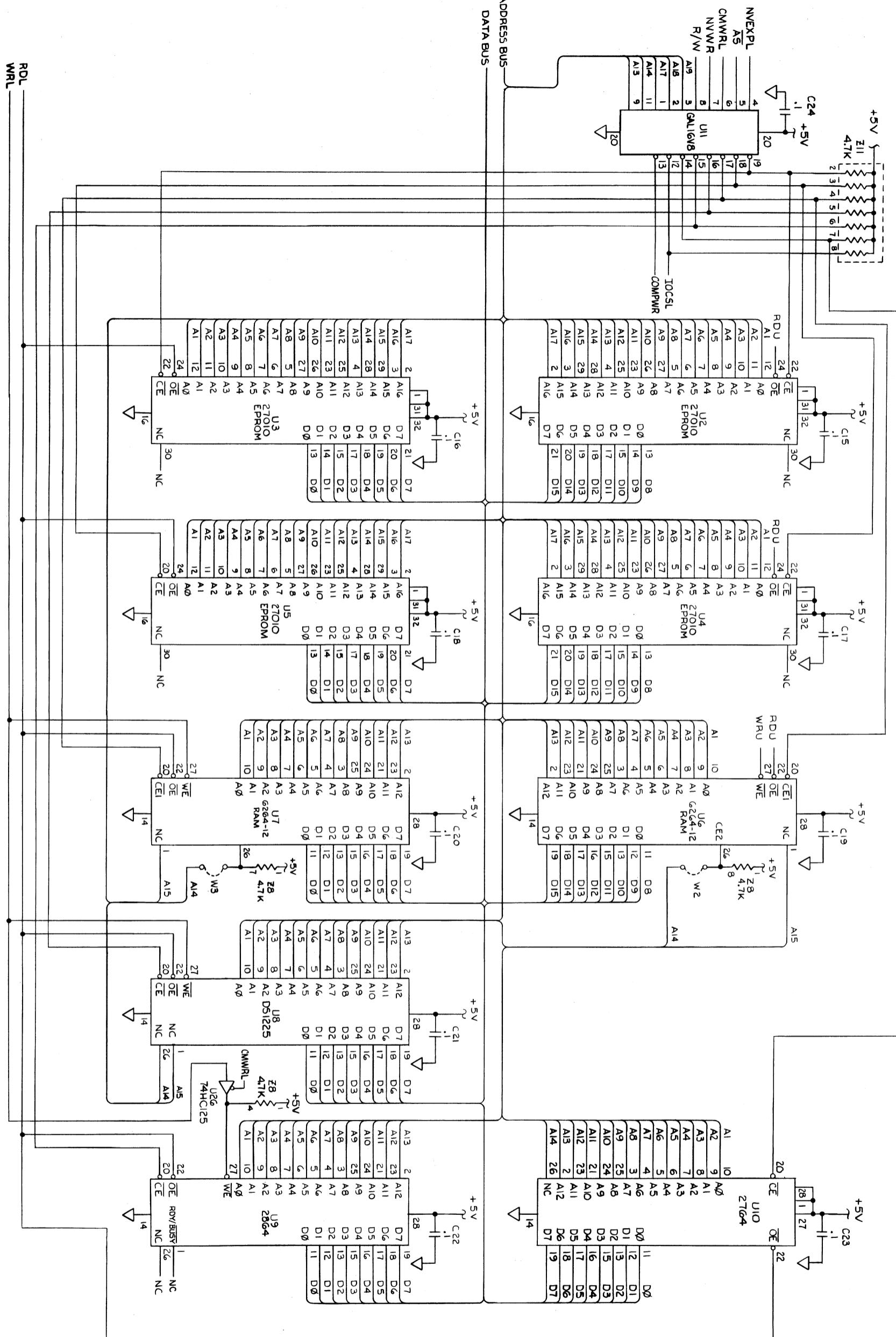
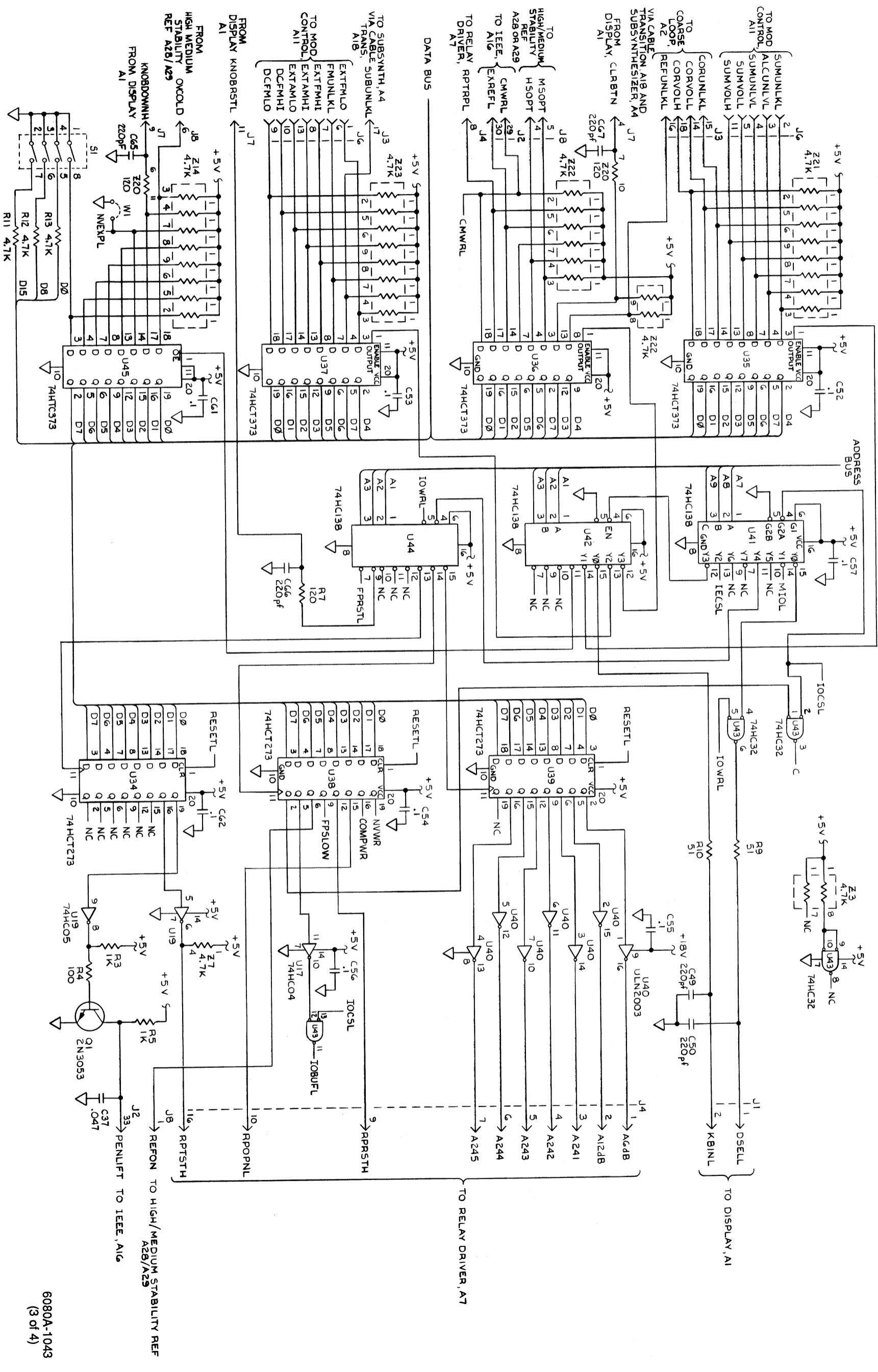
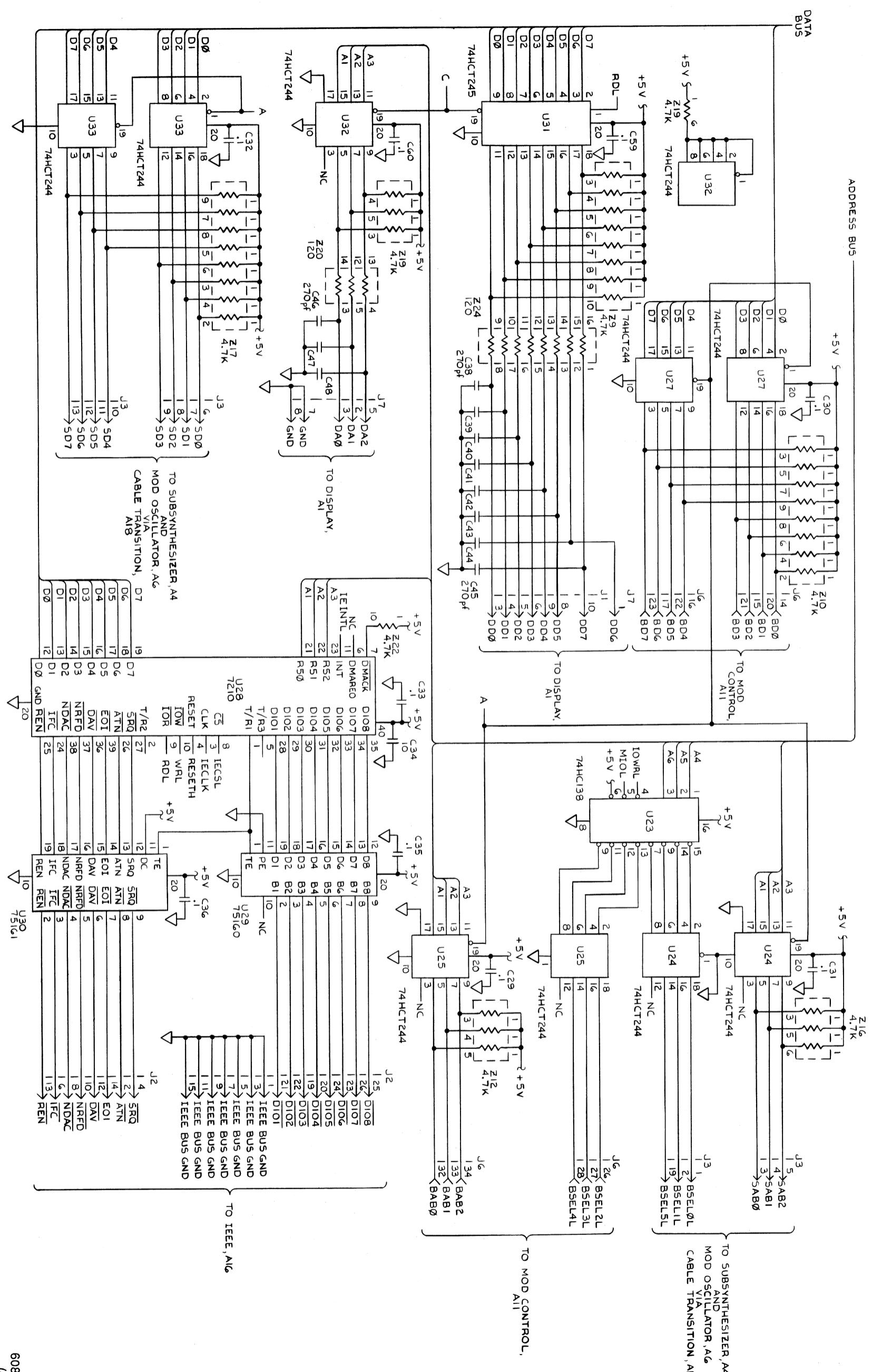


Figure 8-13. A13 Controller PCA (cont)



SCHEMATIC DIAGRAMS



6080A-1043
(4 of 4)

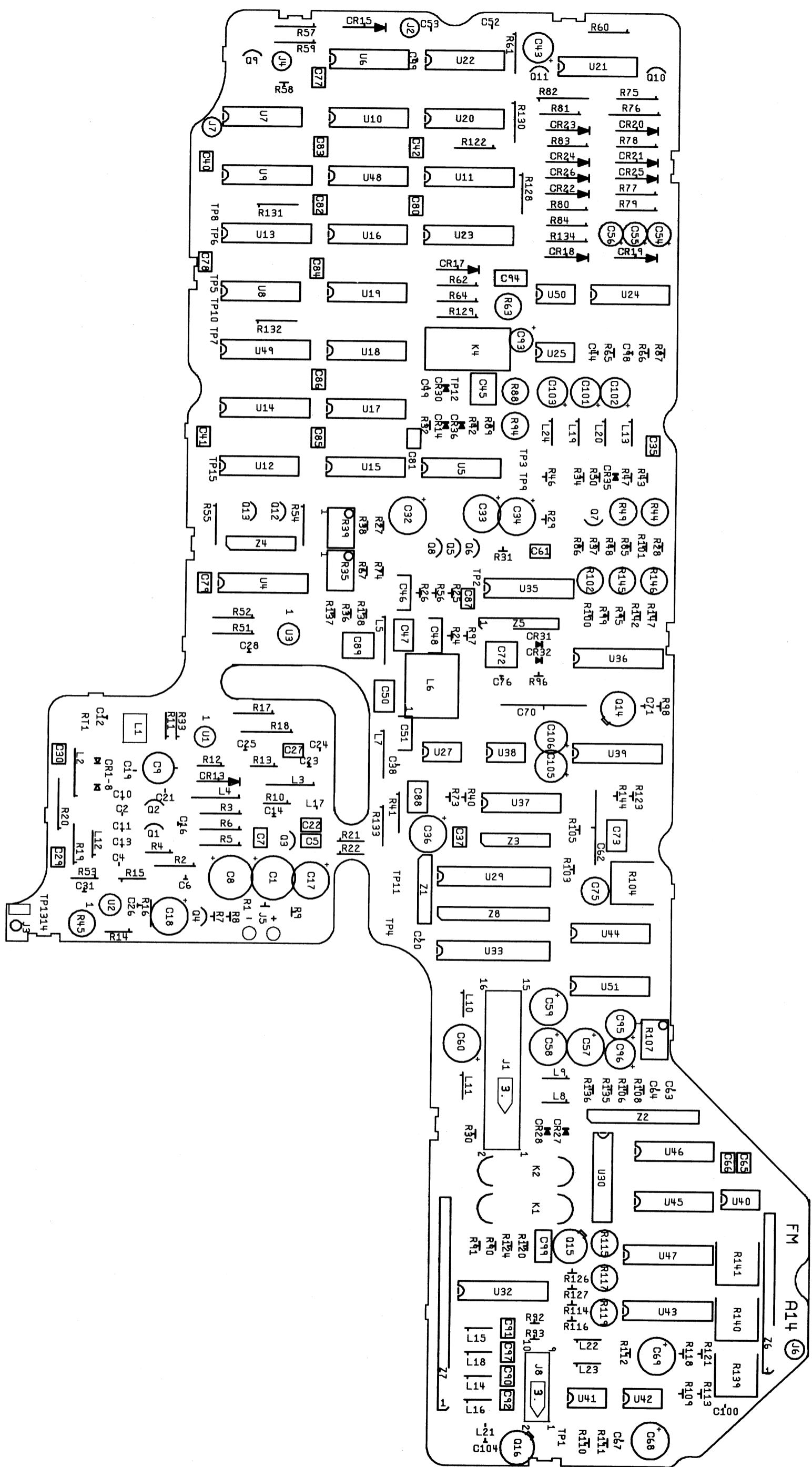


Figure 8-14. A14 FM PCA

Schematic Diagrams

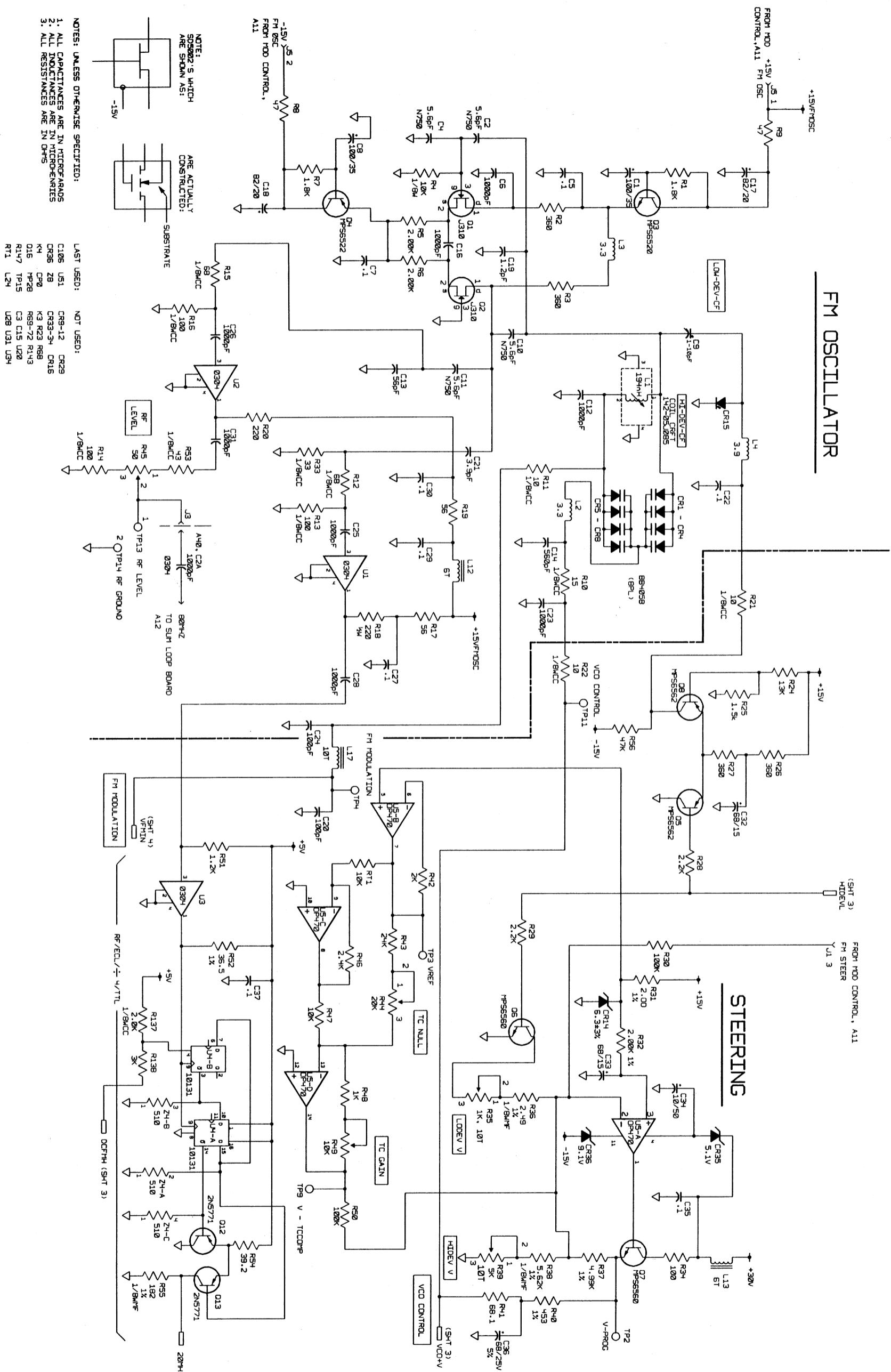


Figure 8-14. A14 FM PCA (cont)

6080A-1045
(1 of 4)

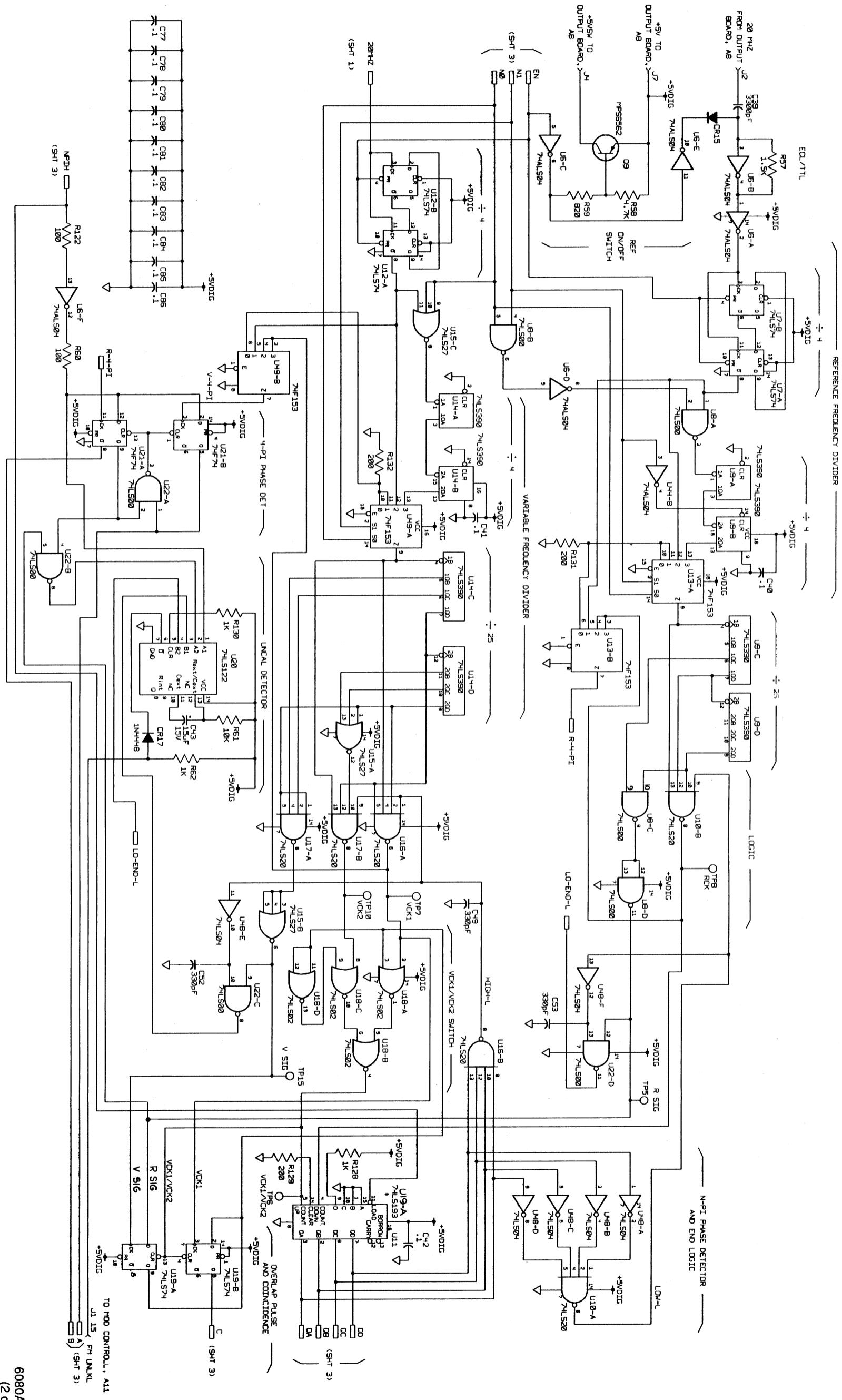
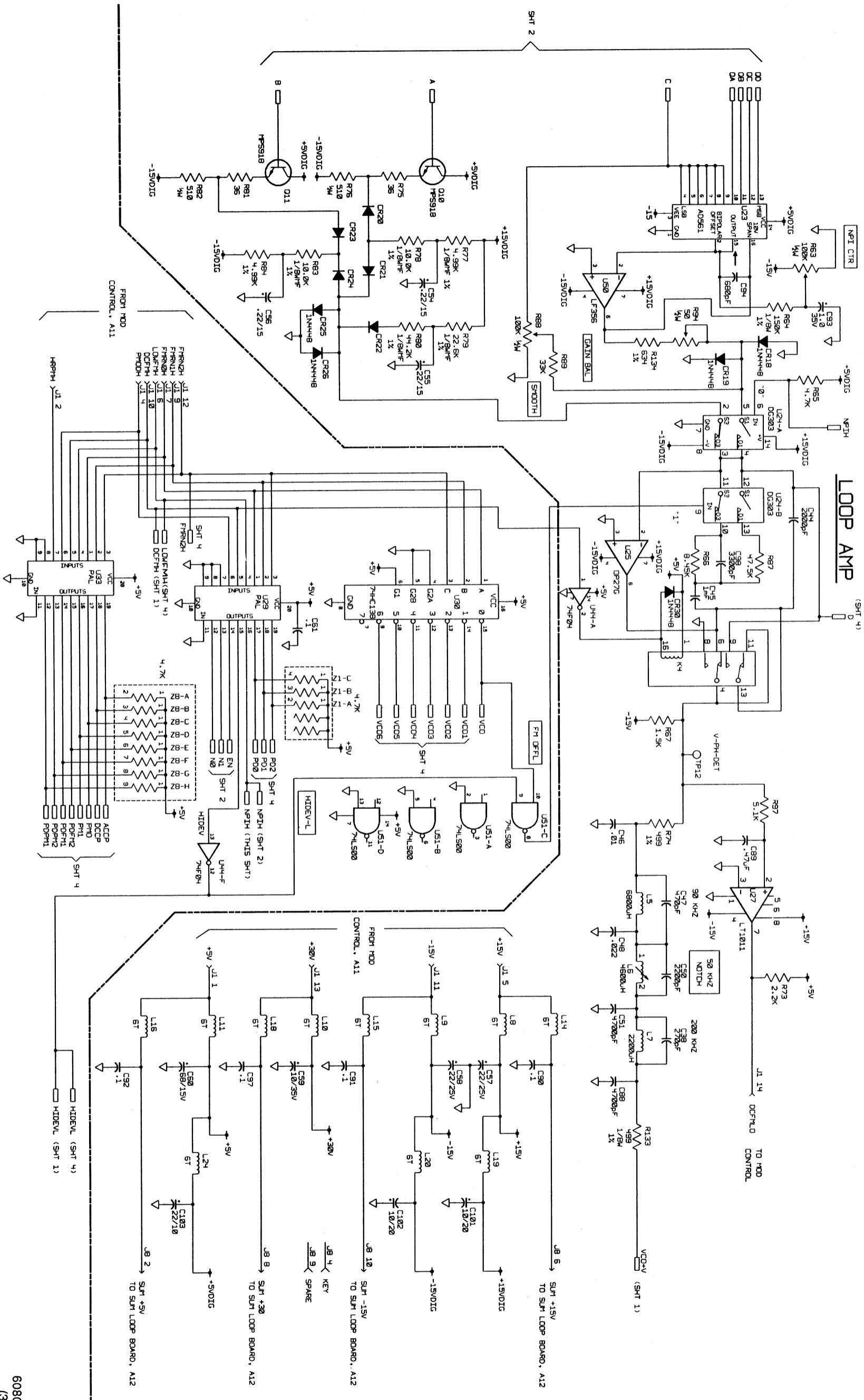


Figure 8-14. A14 FM PCA (cont)

6080A-1045
(2 of 4)

SCHEMATIC DIAGRAMS



SCHEMATIC DIAGRAMS

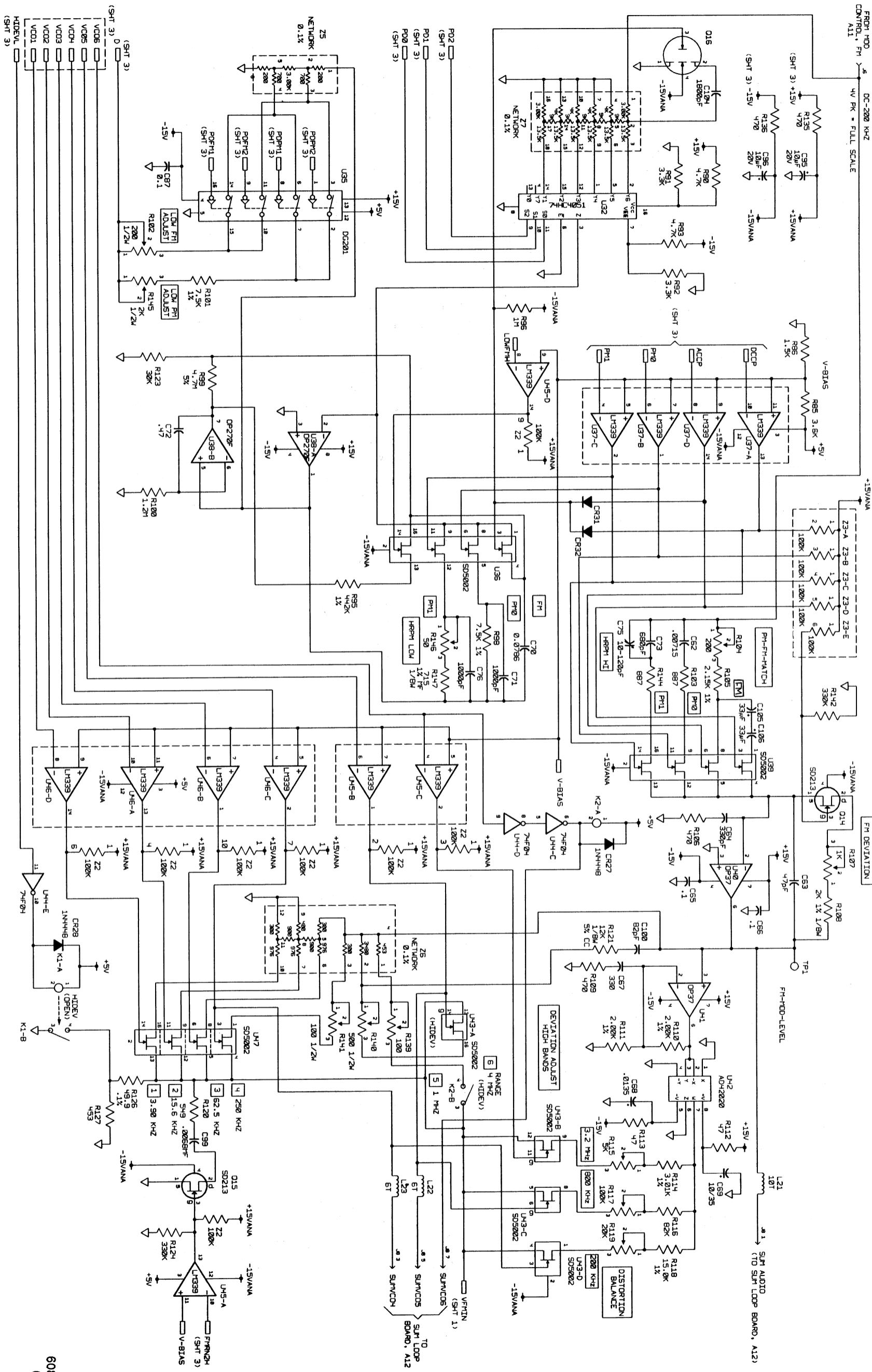
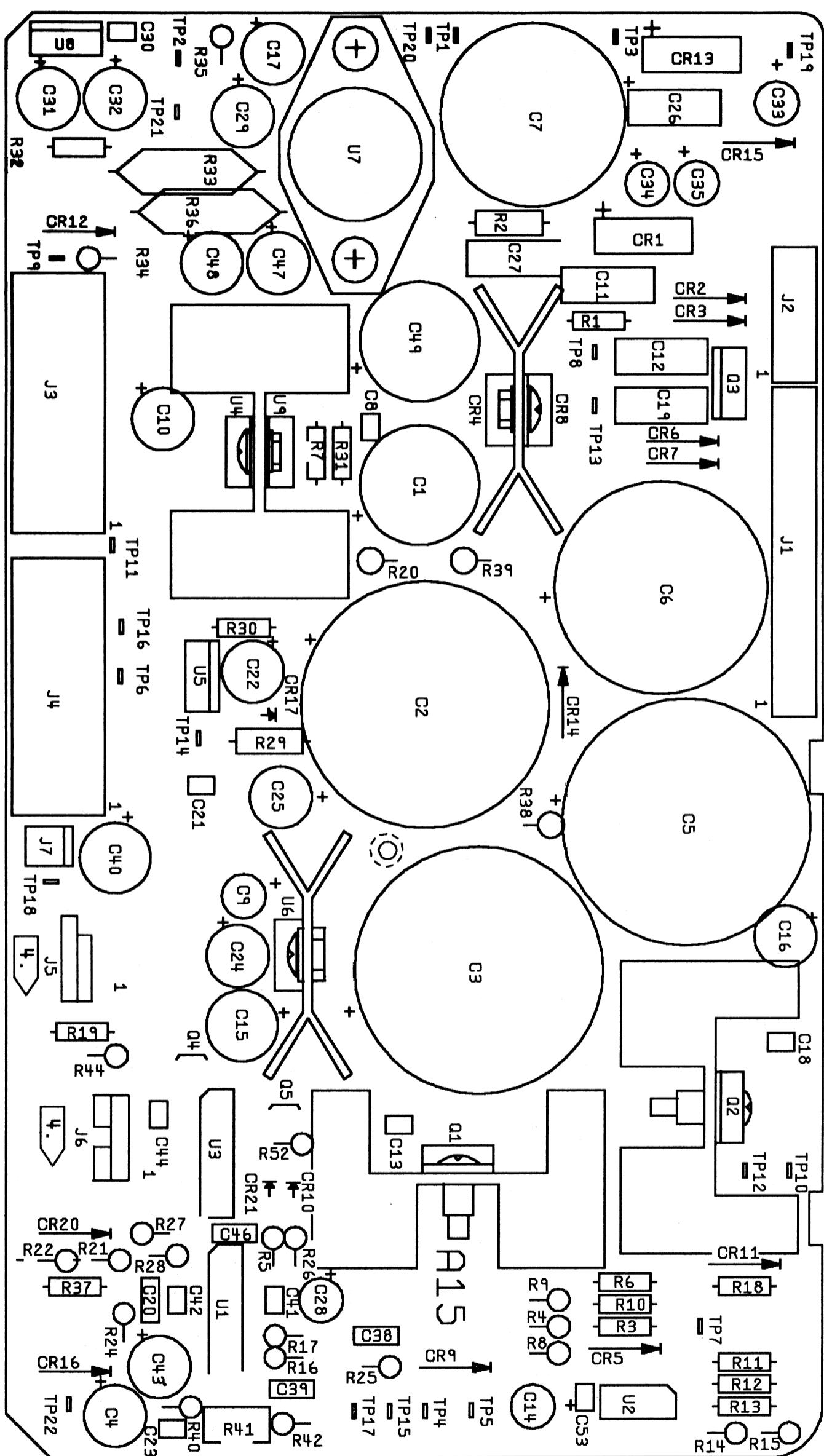


Figure 8-14. A14 FM PCA (cont)

SCHEMATIC DIAGRAMS



SCHEMATIC DIAGRAMS

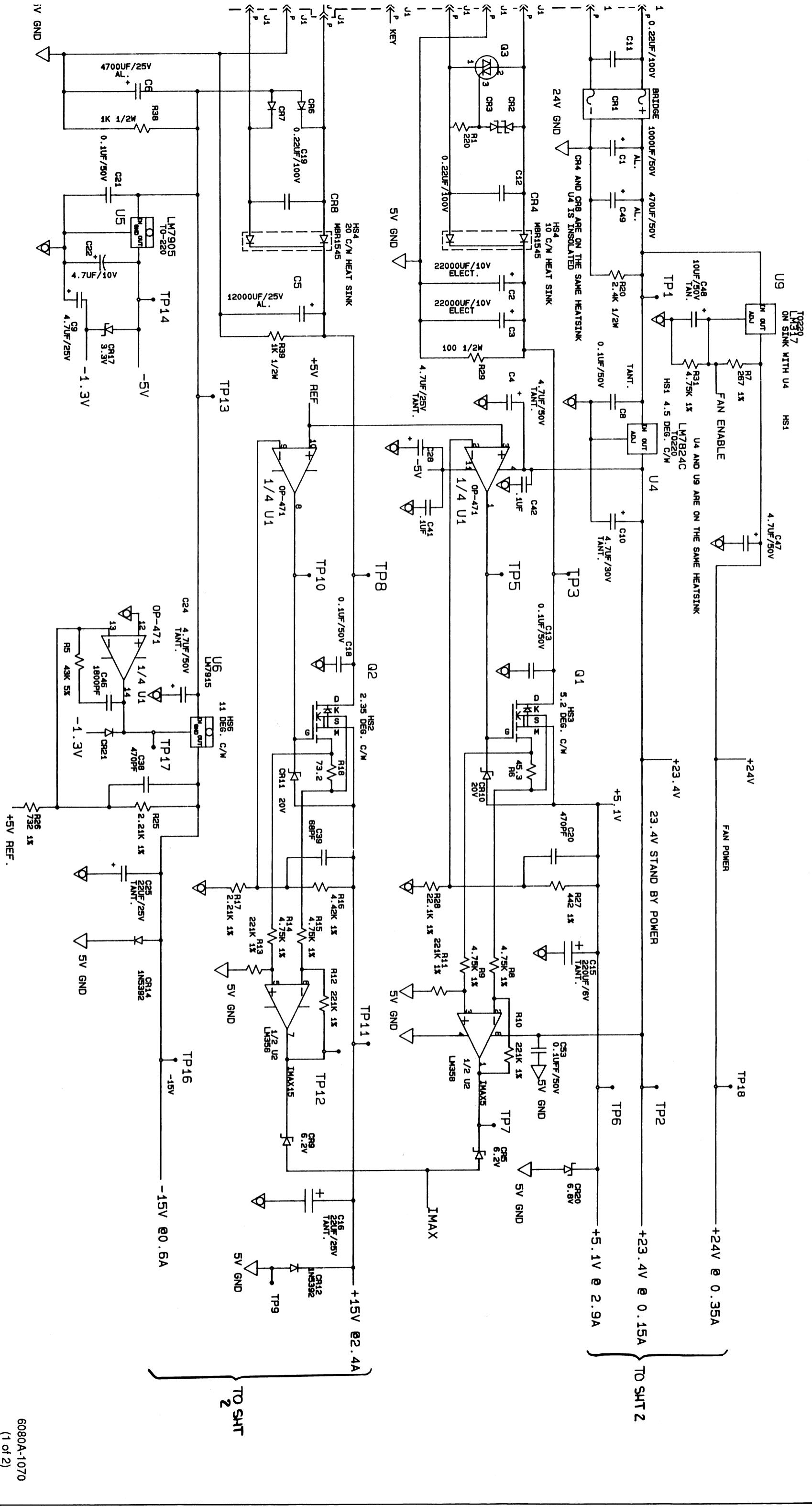
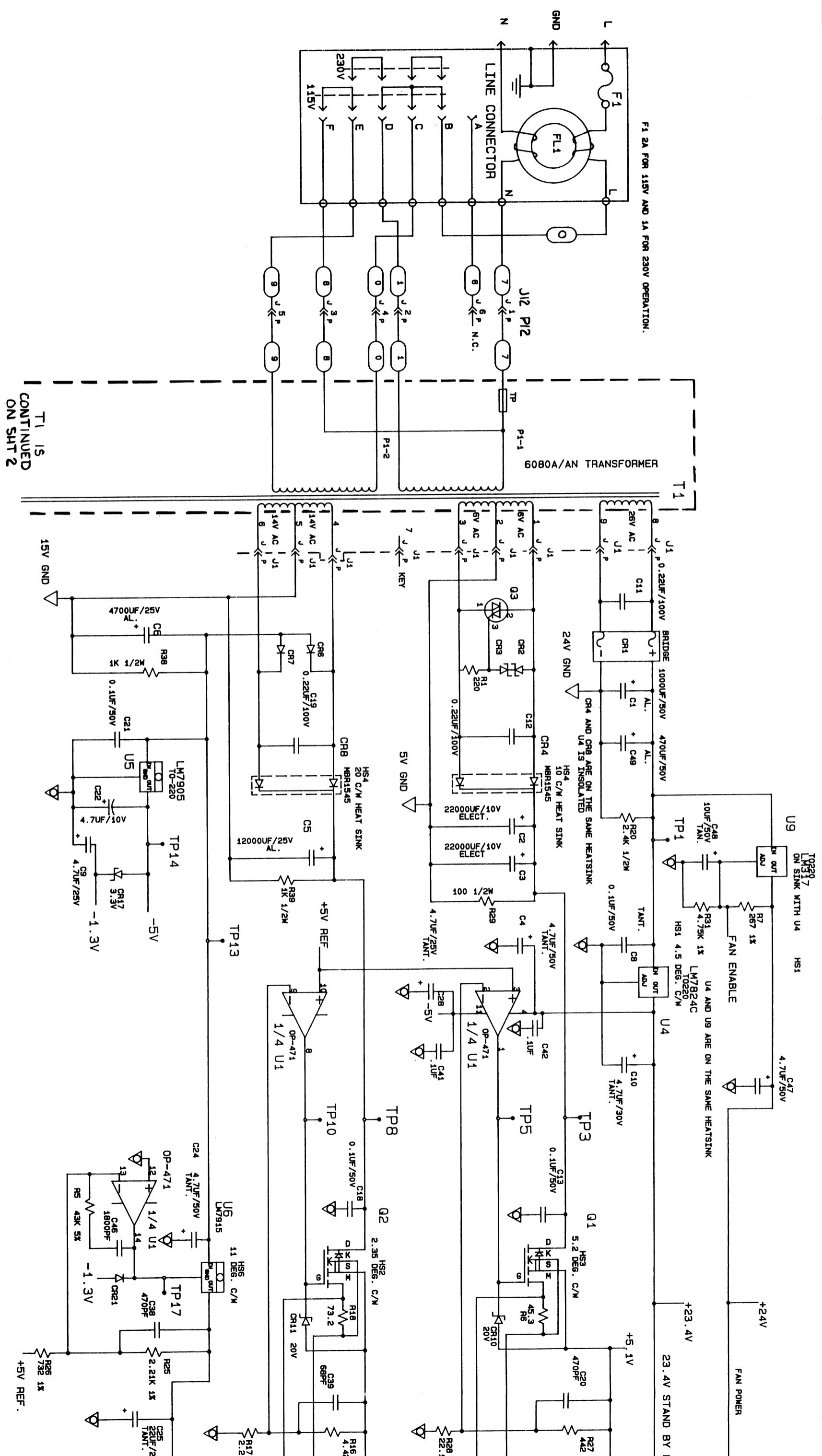


Figure 8-15. A15 Power Supply PCA (cont)



SCHEMATIC DIAGRAMS

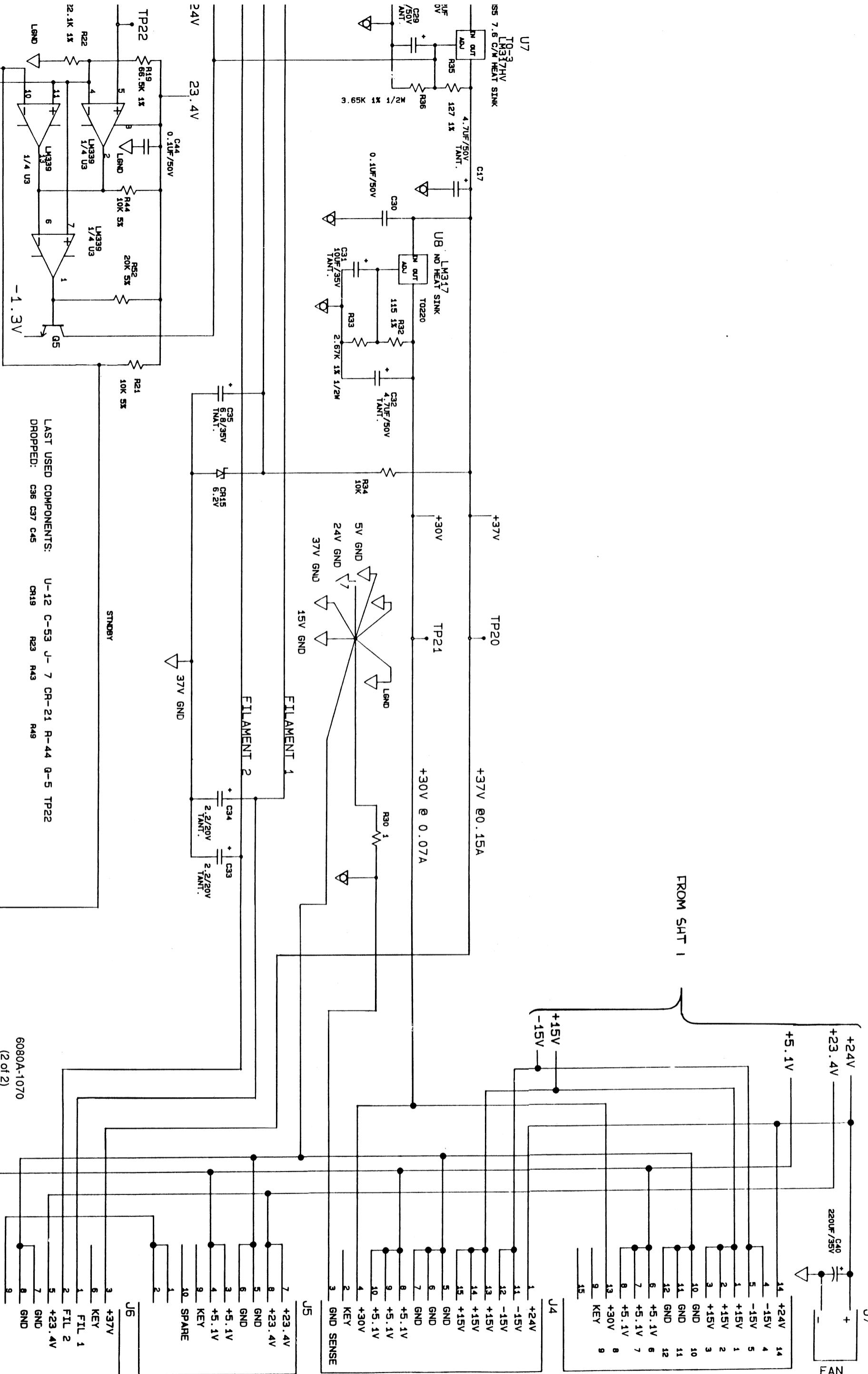
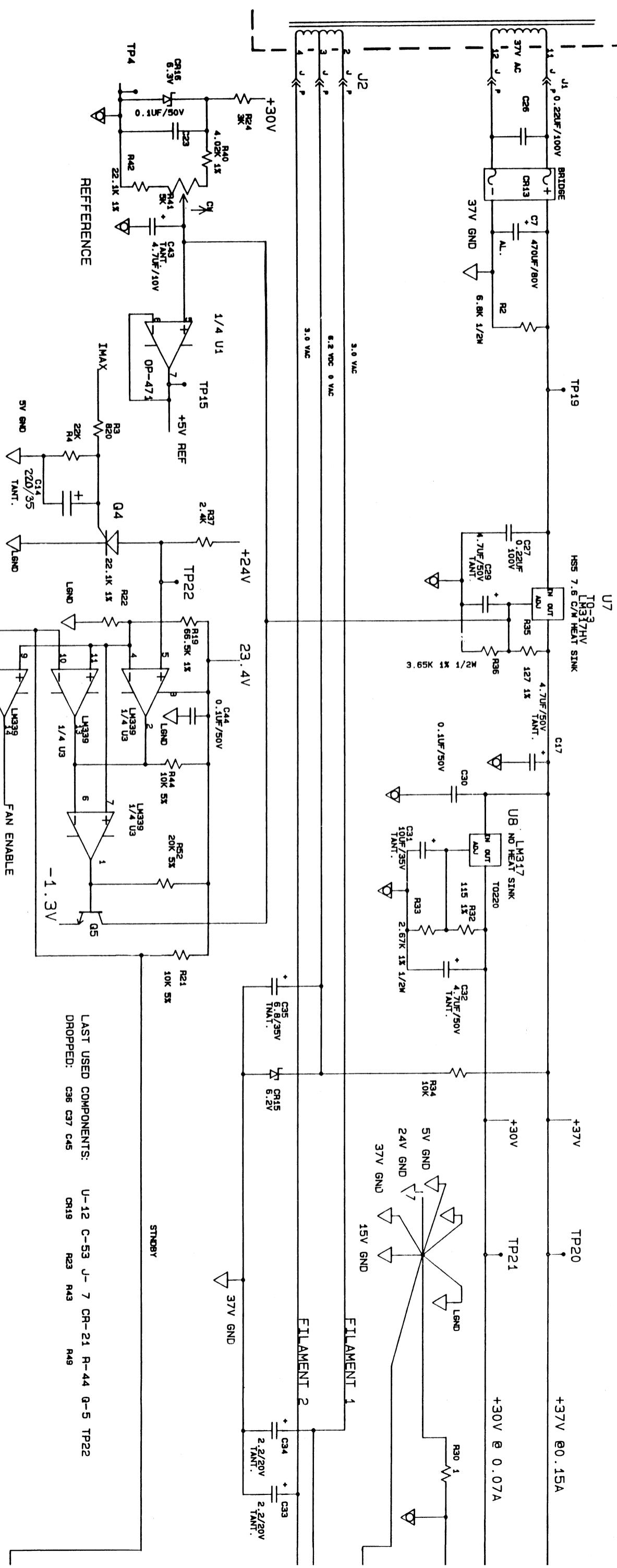


Figure 8-15. A15 Power Supply PCA (cont)

T1 (CONTINUED FROM
SHT I)



NOTES:

1) ALL RESISTORS ARE 1/4W 5% UNLESS OTHERWISE SPECIFIED

LAST USED COMPONENTS:
DROPPED: C36 C37 C45 CR19 R23 R43 R49

U-12 C-53 J-7 CR-21 R-44 Q-5 TP22

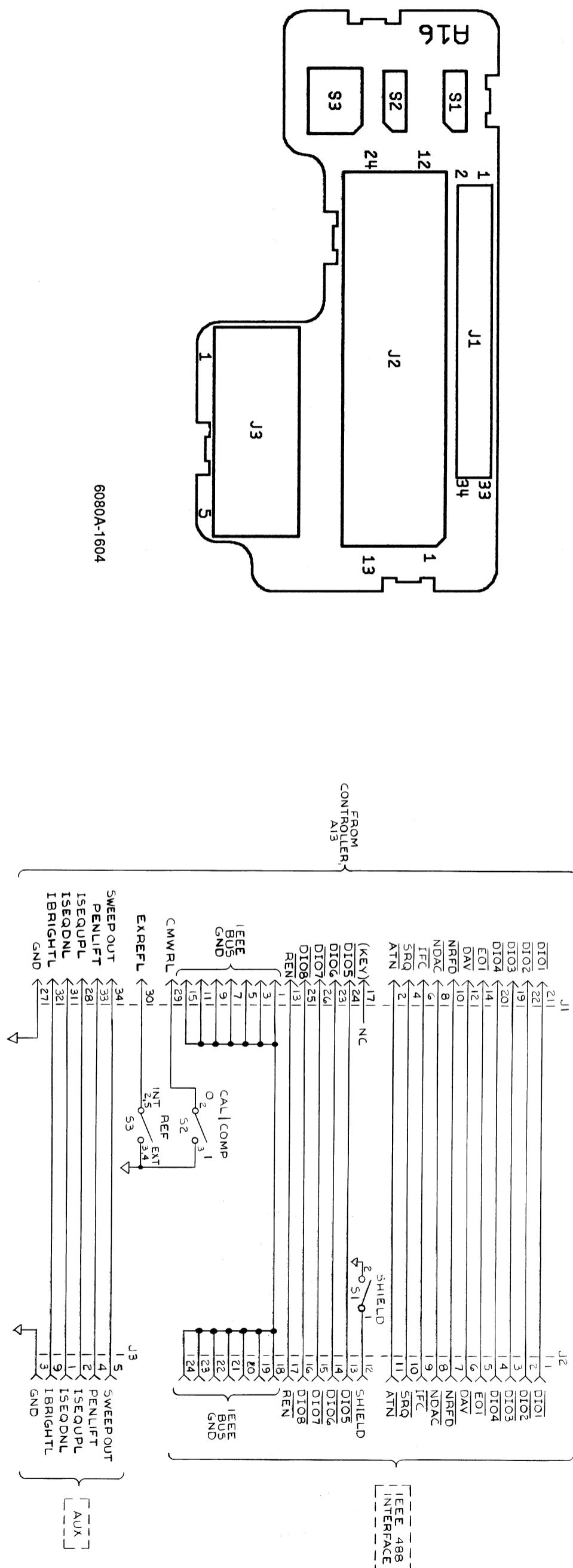
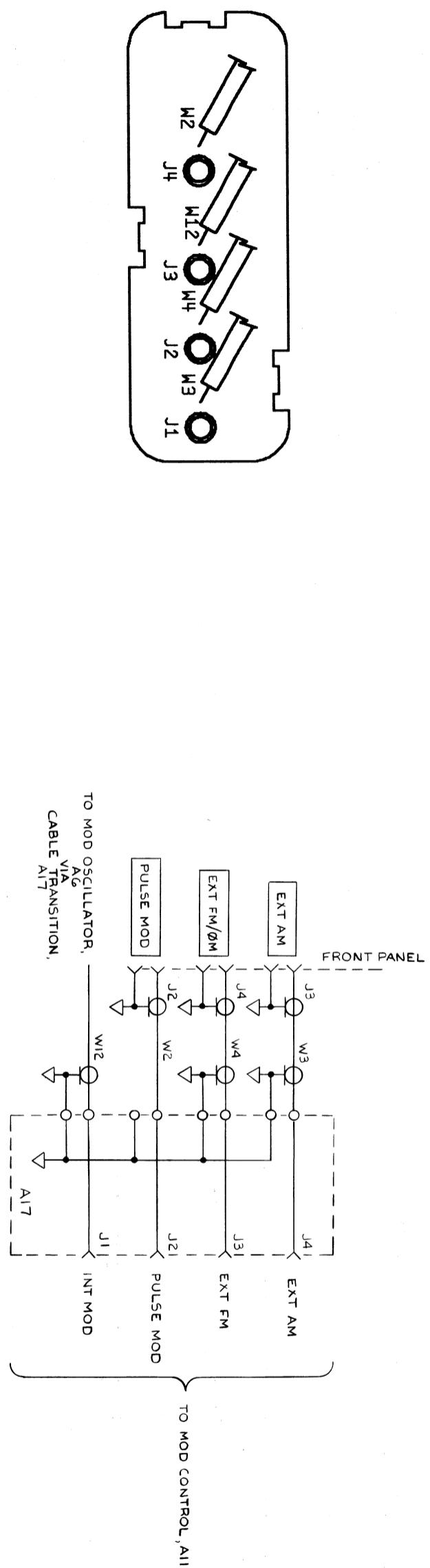


Figure 8-16. A16 IEEE Connector PCA

6080A-1071

SCHEMATIC DIAGRAMS



6080A-1610

6080A-1044

Figure 8-17. A17 Cable Transition PCA

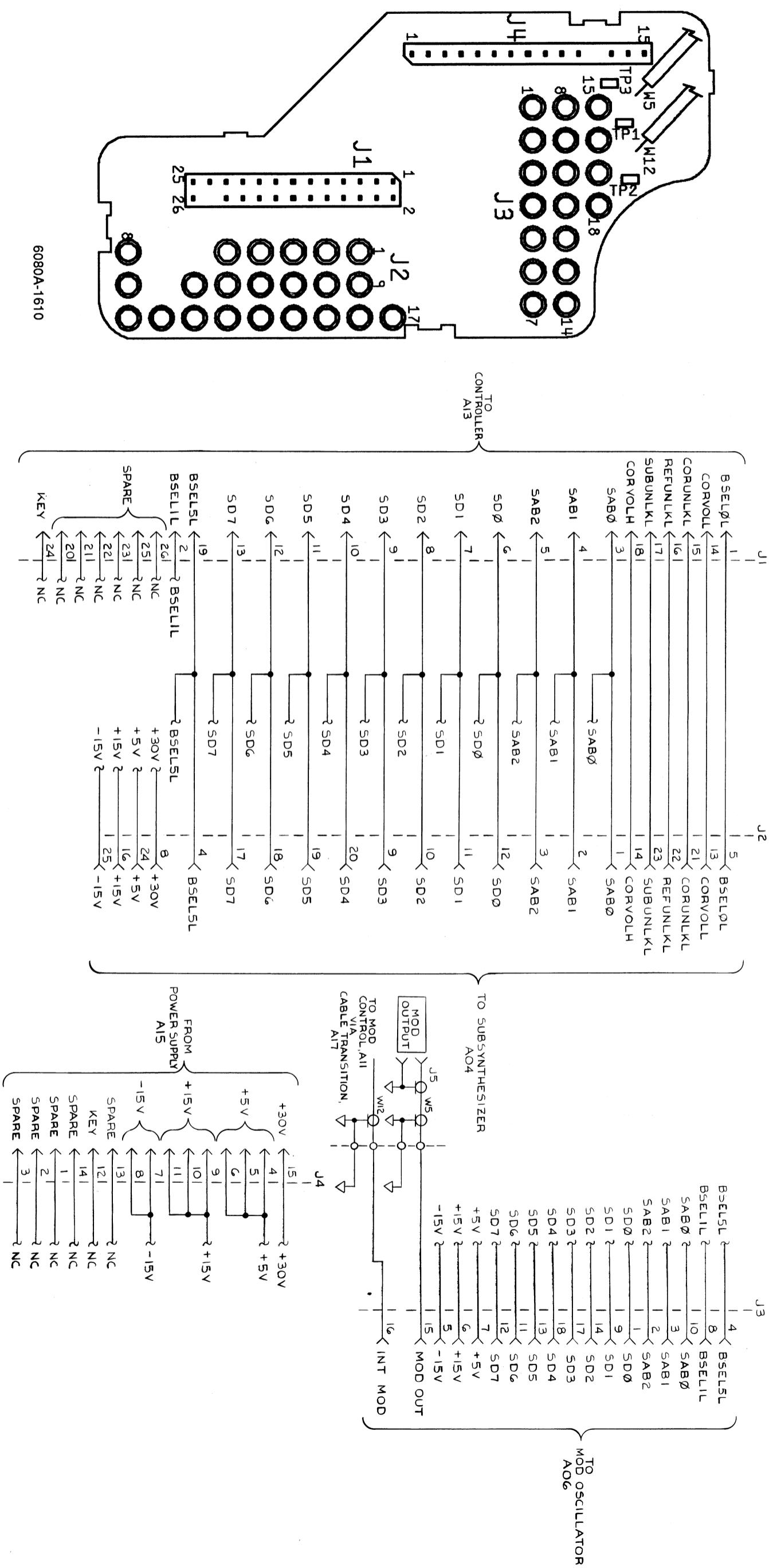


Figure 8-18. A18 Cable Transition PCA

SCHEMATIC DIAGRAMS

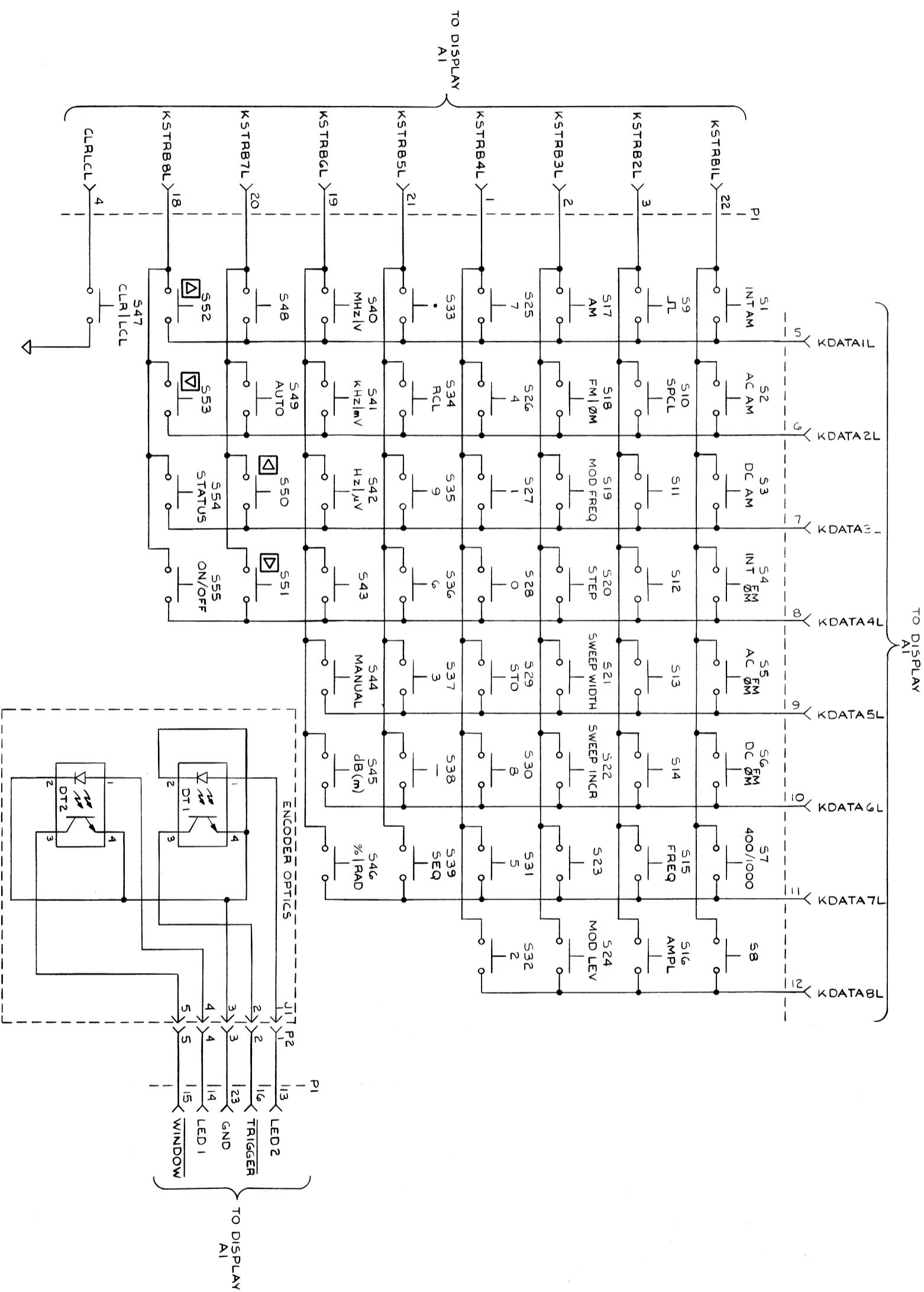
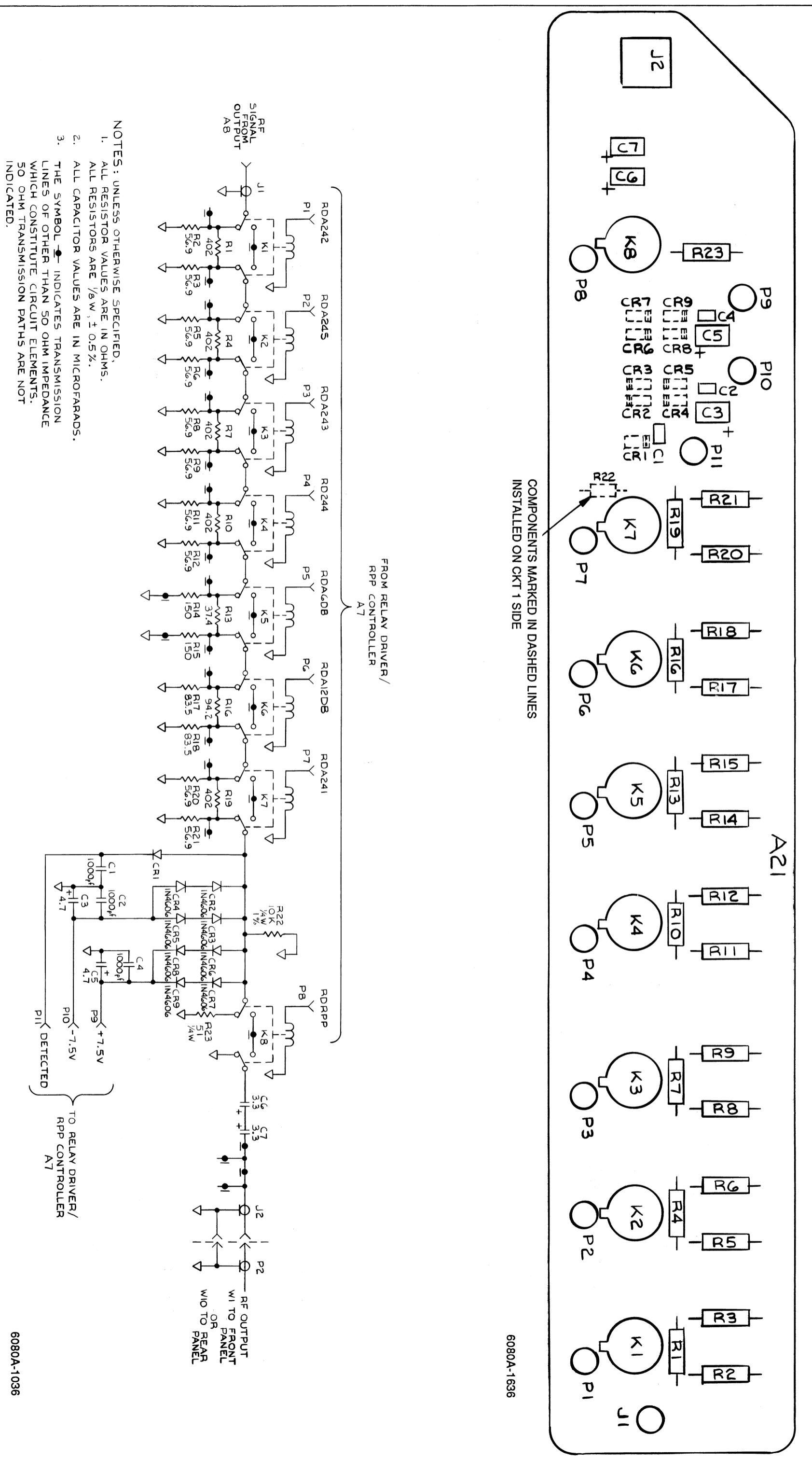


Figure 8-19. A19 Switch PCA



SCHEMATIC DIAGRAMS

